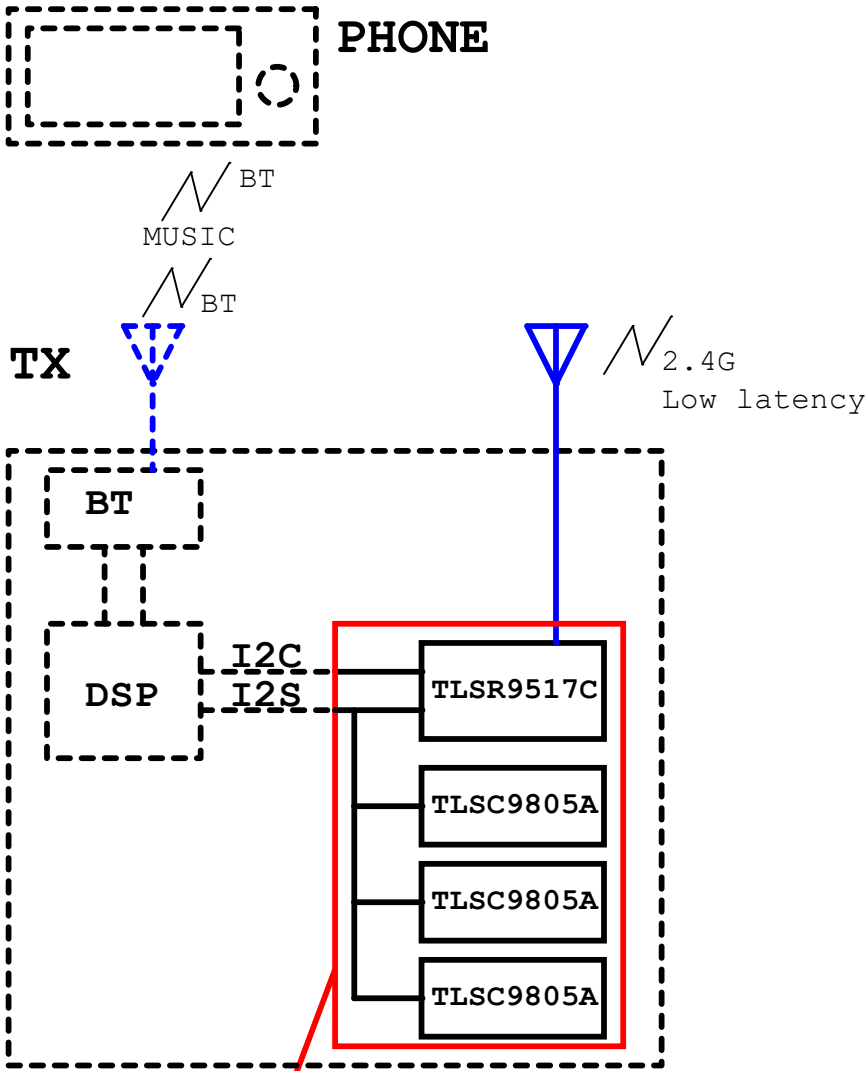
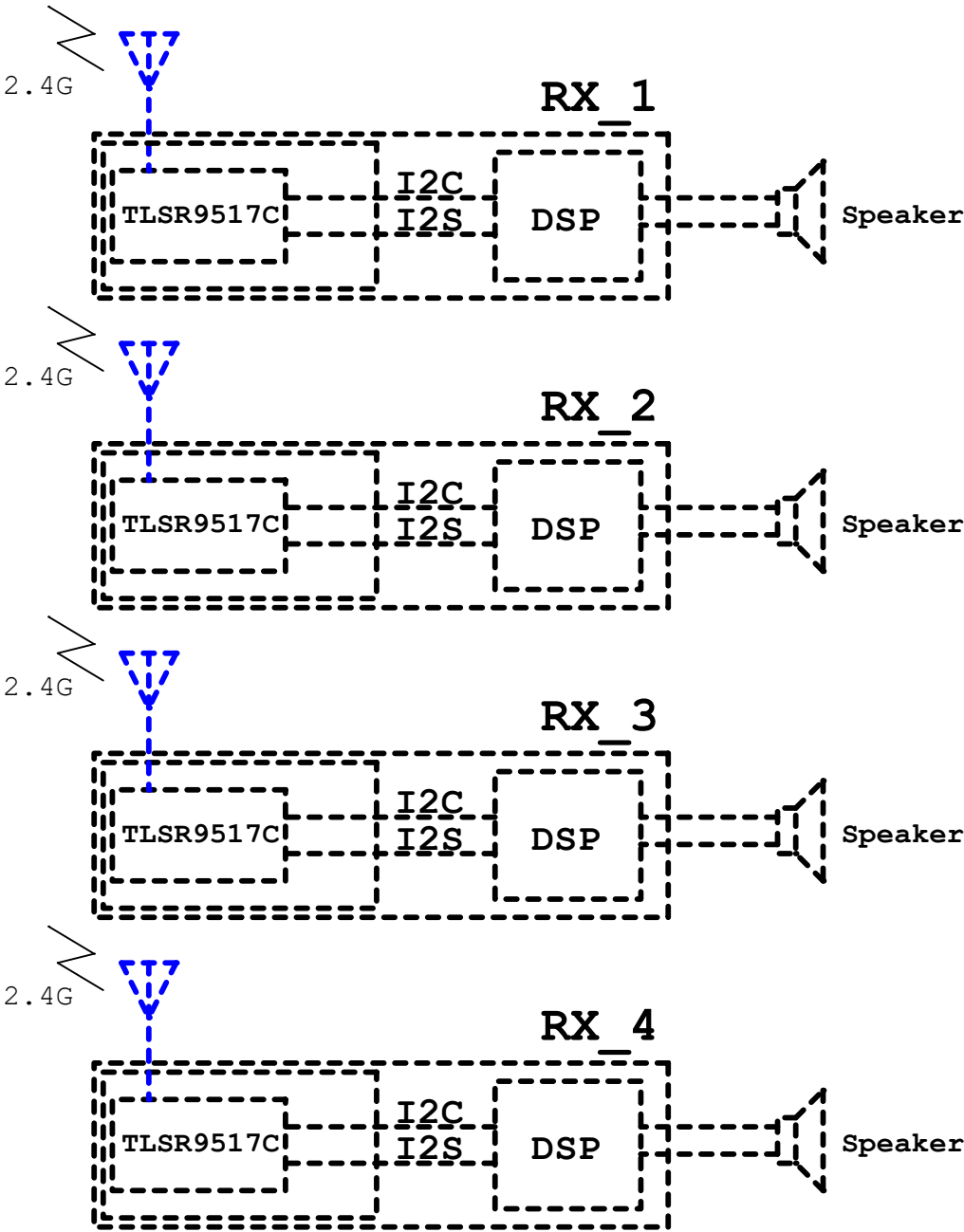


Soundbar 1 to 6.1 Solution



Module_A

See the schematic diagram for the detailed design of the red box.



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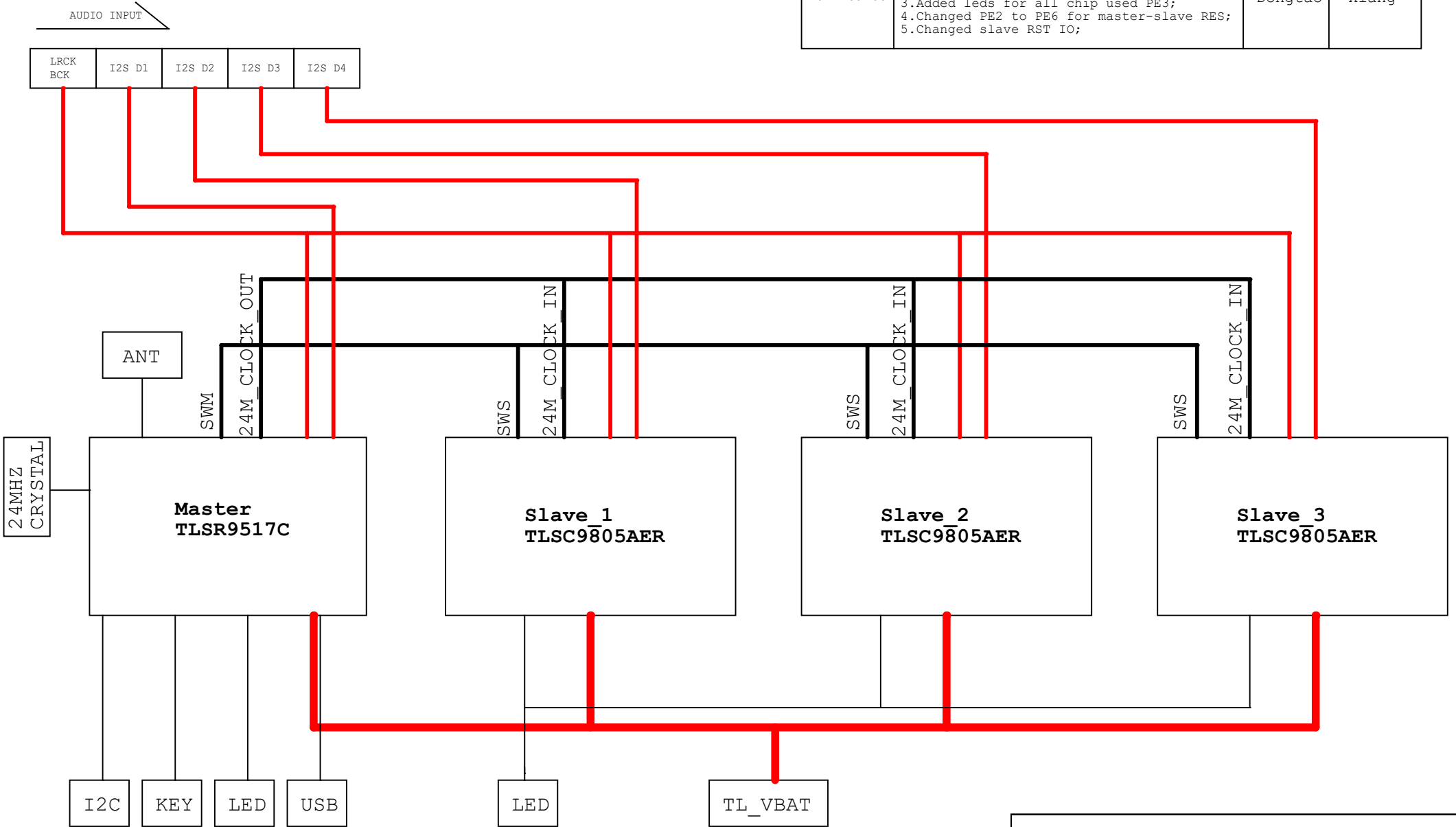
Module_A

Block diagram

REV1.0

REV1.1

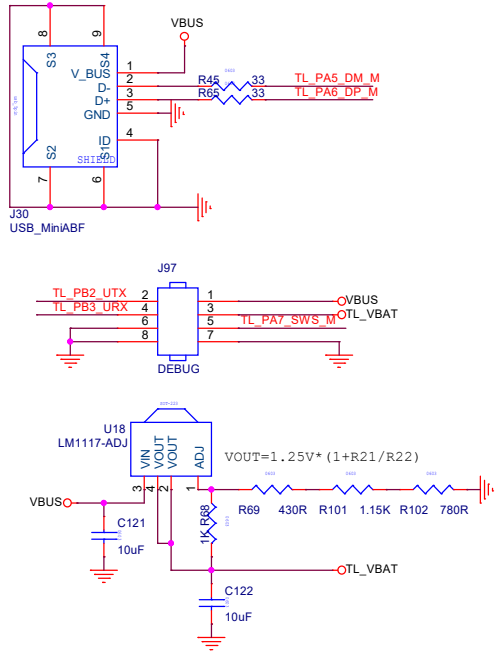
Date	Note	Designer	Reviewer
2021-08-05	Initial Version	HongWei	Xiang
2021-09-08	1.Remove some test points; 2.Audio line in to 3 slave chip; 3.Added leds for all chip used PE3; 4.Changed PE2 to PE6 for master-slave RES; 5.Changed slave RST IO;	Dongtao	Xiang



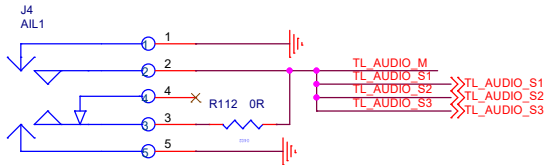
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Master

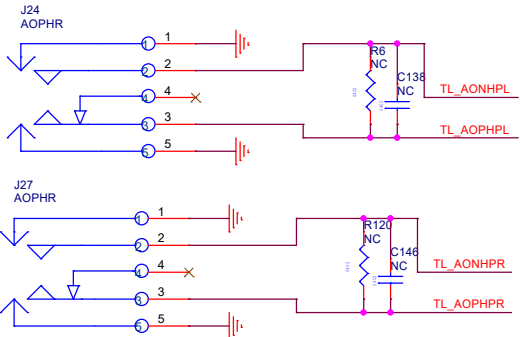
DEBUG



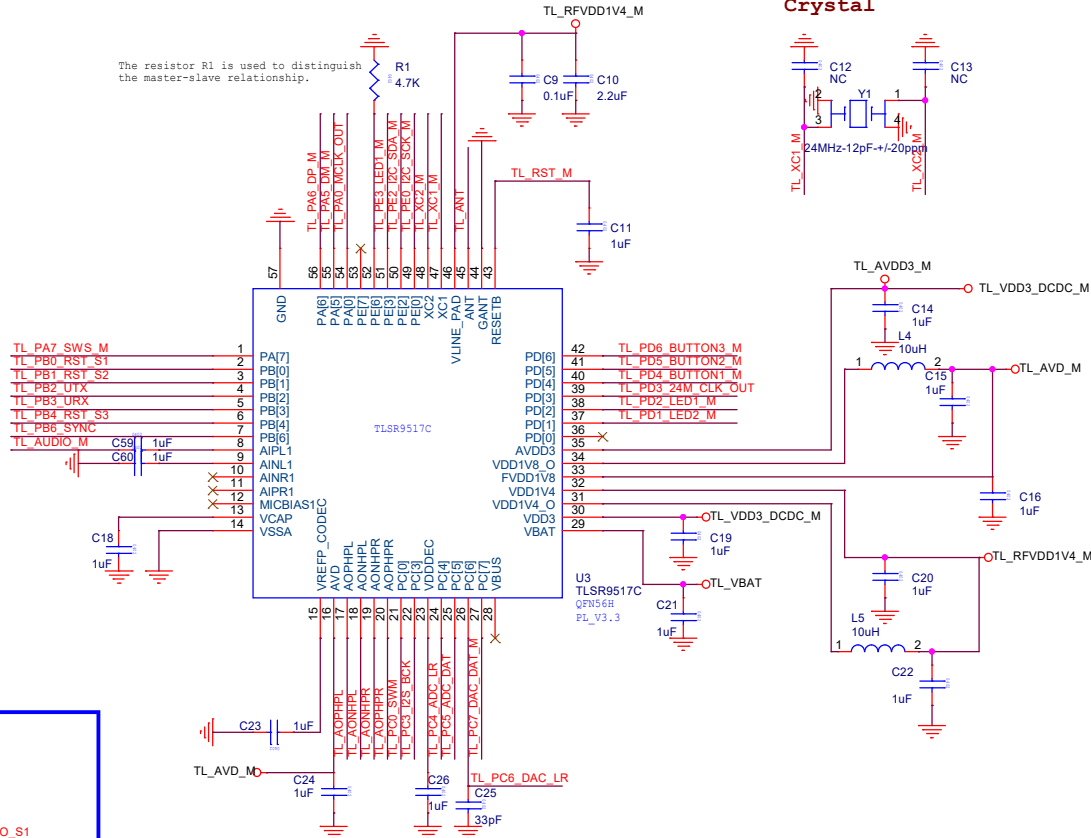
Audio line in



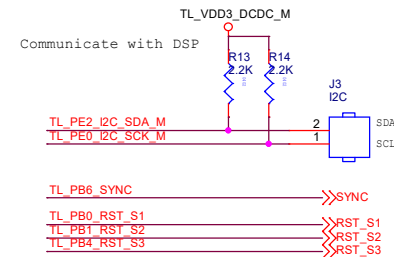
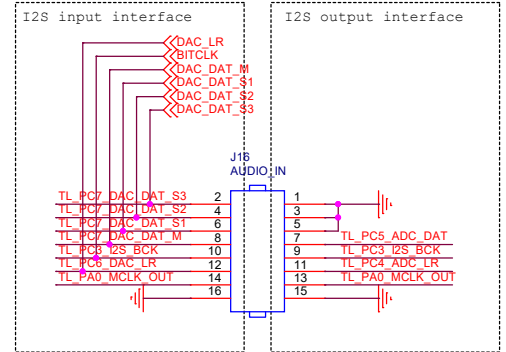
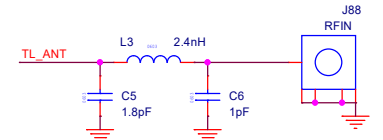
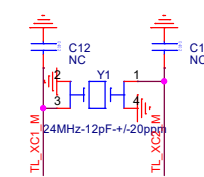
Audio line out



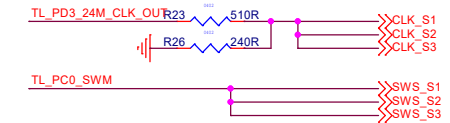
The resistor R1 is used to distinguish the master-slave relationship.



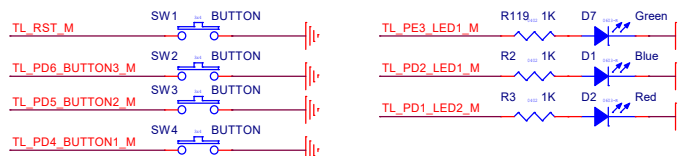
Crystal



The master provides a 24M clock for the slave. The slave terminal receives about 1.2V, and the IO voltage is 3.3V, so it needs to be divided into 1/3.

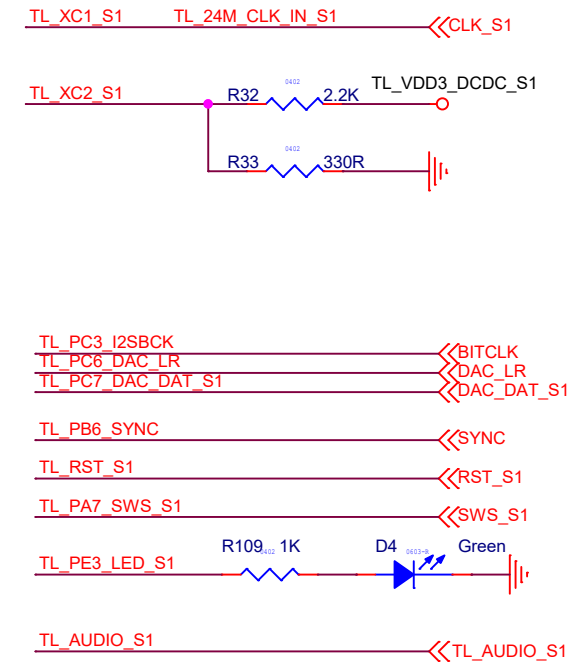
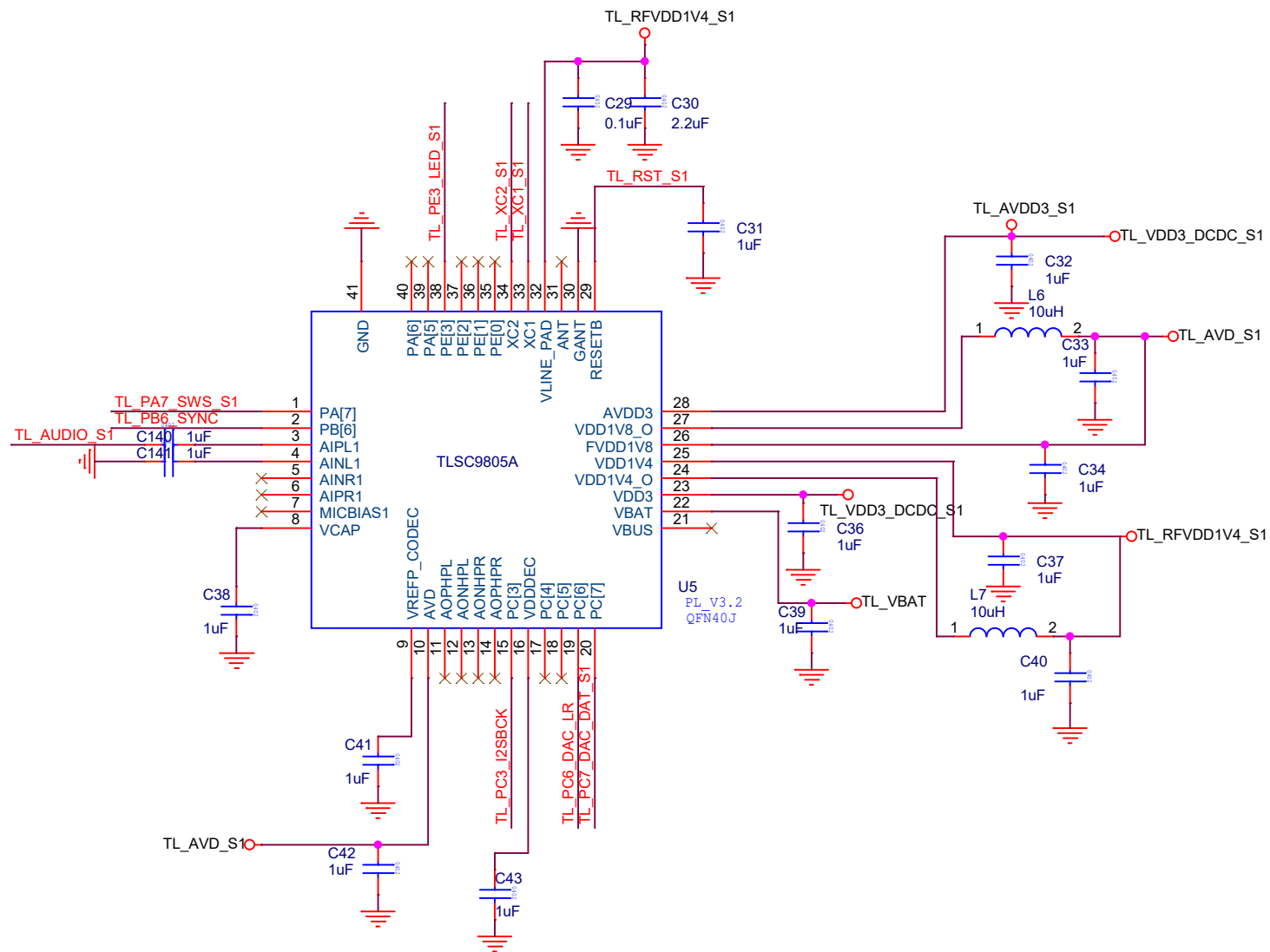


KEY&LED



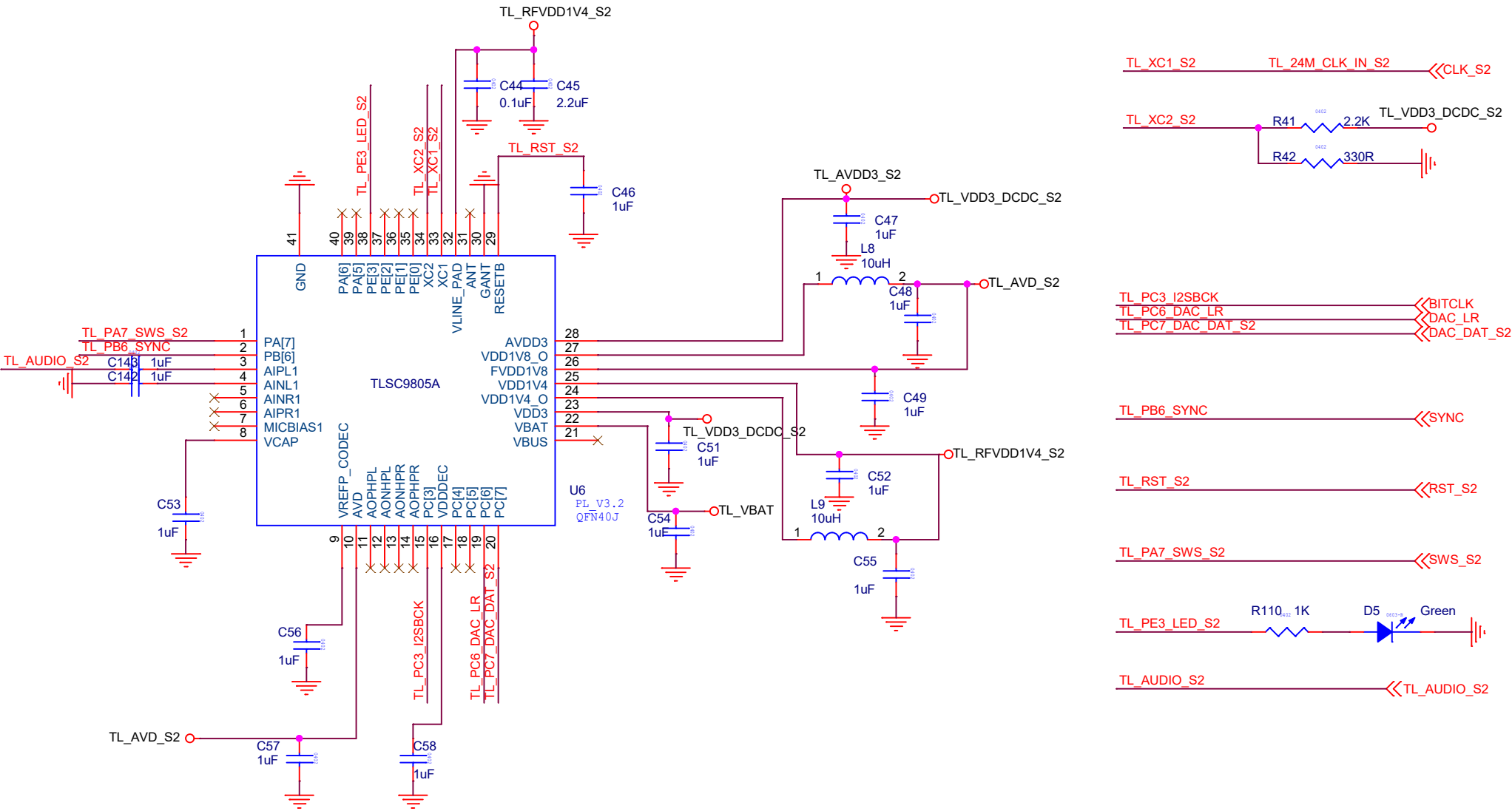
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Slave S1



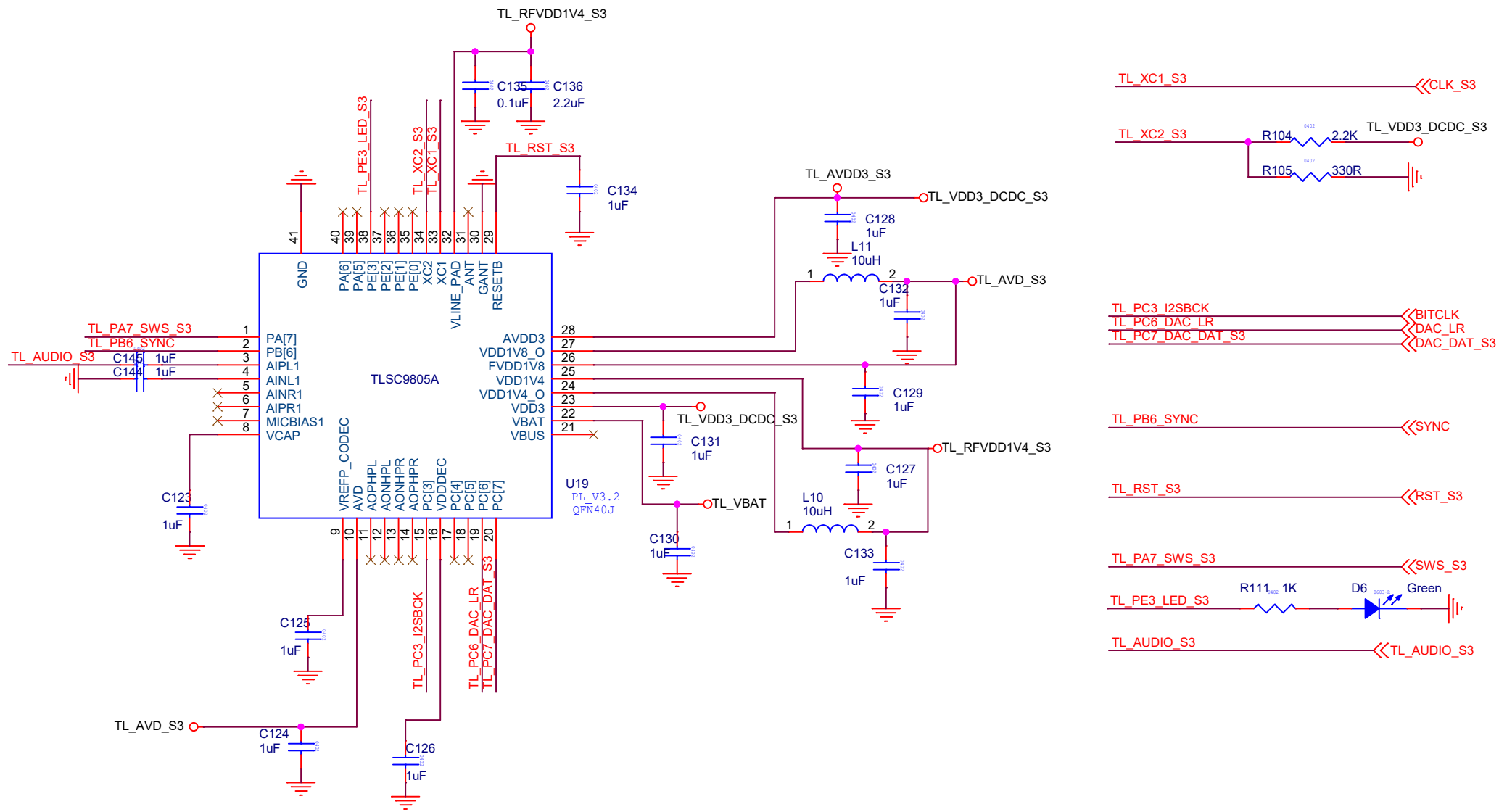
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Slave_S2



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Slave_S3



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