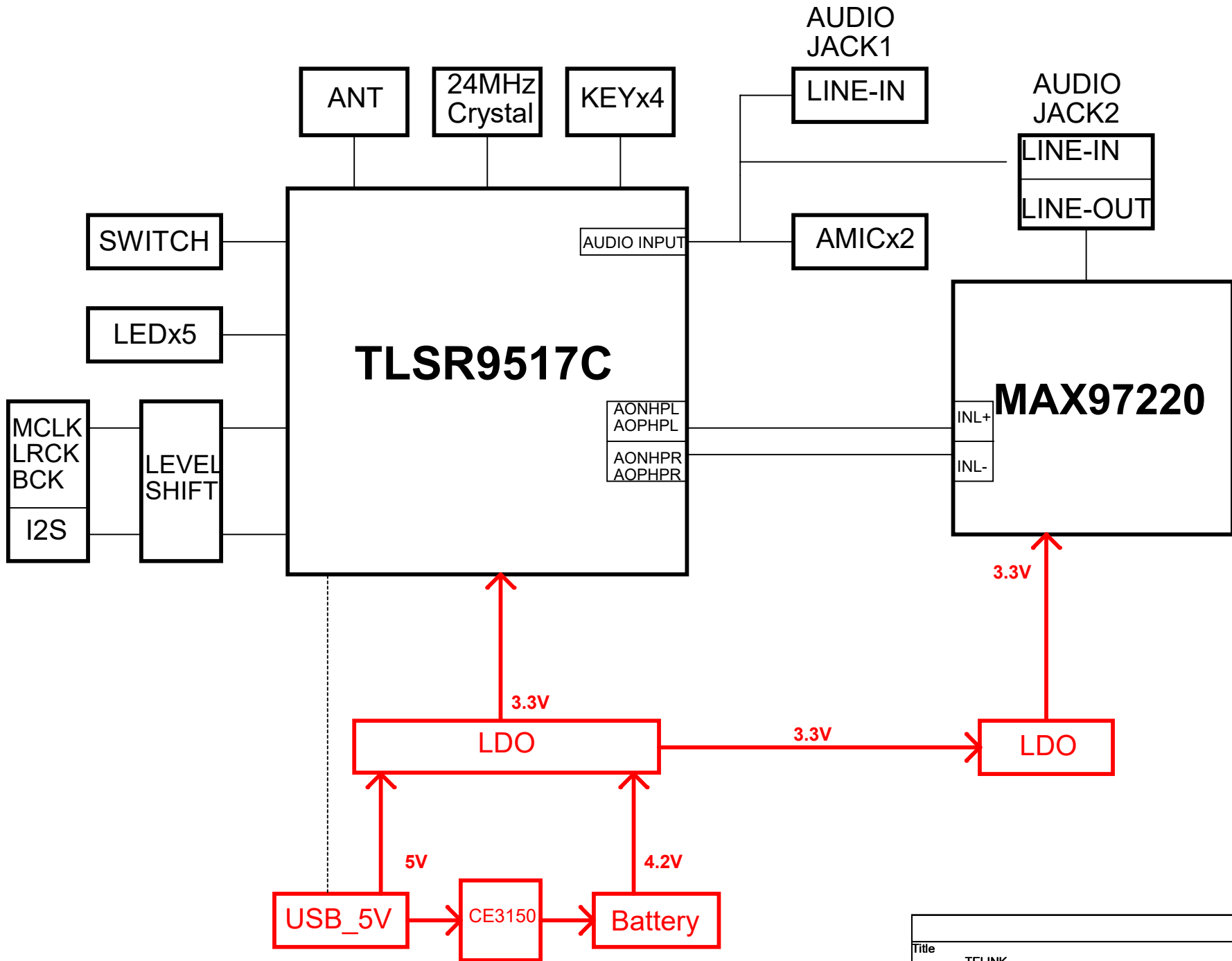
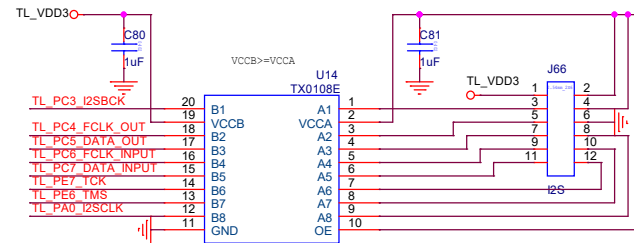
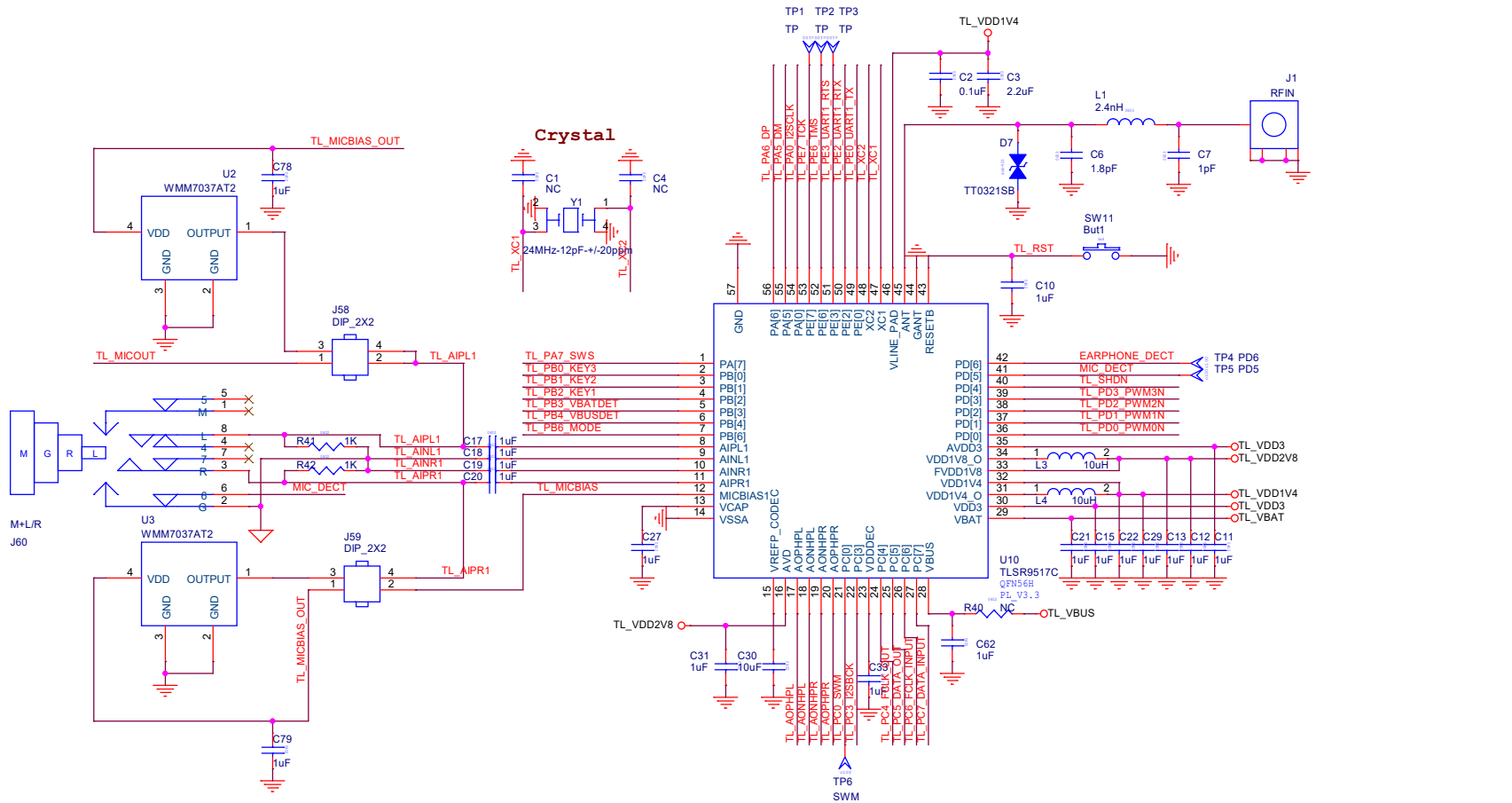
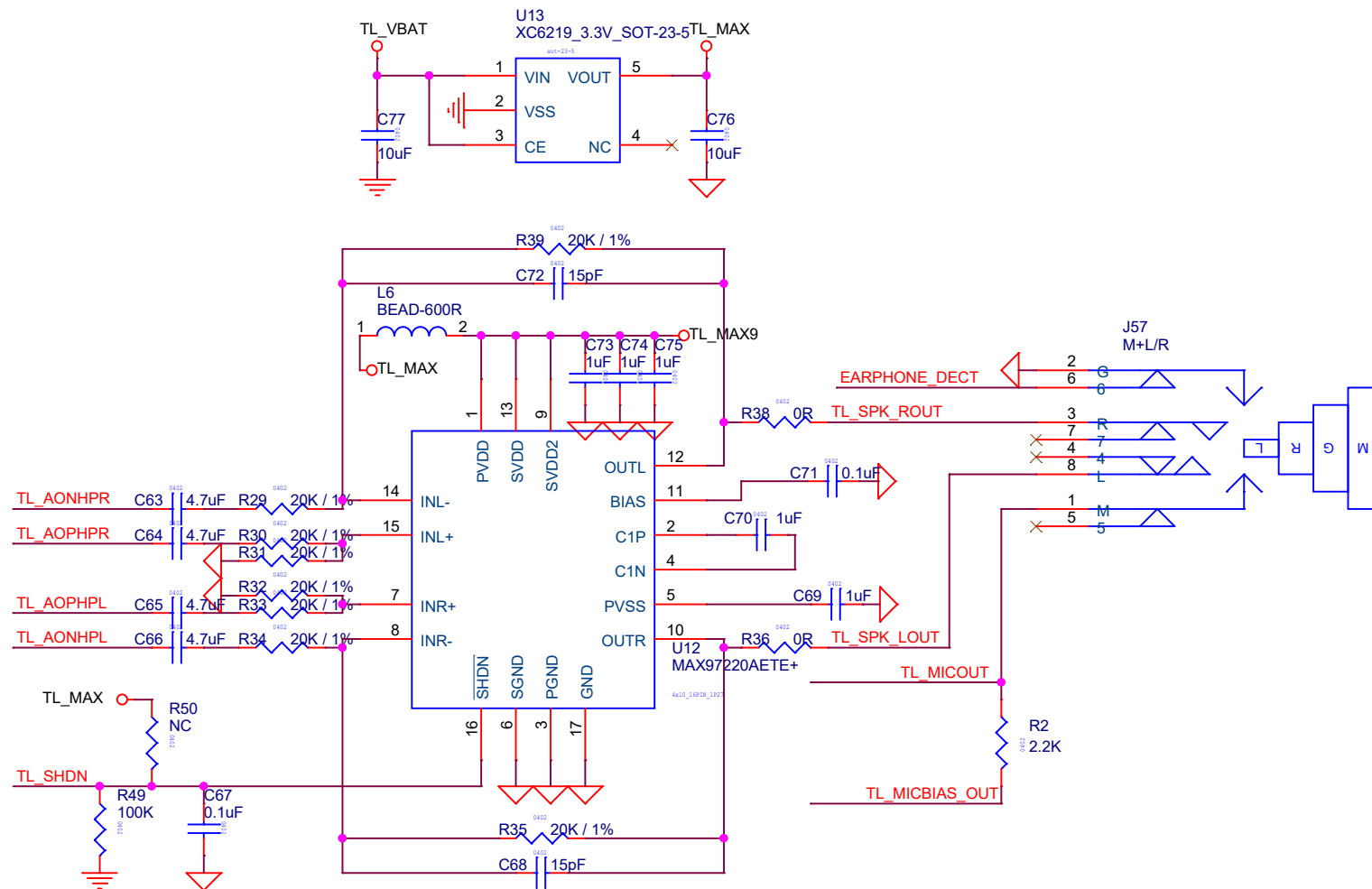


Date	Version	Note	Designer	Reviewer
2021-07-13	V1.2	1. TLSR9517C 2. AUDIO INPUT: USB IN / DUAL-LINEIN / DUAL- MIC 3. AUDIO OUTPUT: I2S / DUAL-SPK 4. I2S LEVEL SHIFT	Xiang	Weixiang
2021-09-18	V1.3	1. TLSR9517C 2. change C63/C64/C65/C66 to 4.7uF 3. change R29/R30/R31/R32/R33/R34/R35/R39 to 20K/1% 4. add R50 0R to TL_MAX	zhenchuang	Xiang





- ⟨⟨TL_AONHPR
- ⟨⟨TL_AOPHPR
- ⟨⟨TL_AOPHPL
- ⟨⟨TL_AONHPL
- ⟨⟨TL_SHDN
- ⟨⟨TL_MICBIAS_OUT
- ⟨⟨TL_MICOUT
- ⟨⟨EARPHONE_DECT



Title TELINK				
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- <<TL_PD3_PWM3N
- <<TL_PD2_PWM2N
- <<TL_PD1_PWM1N
- <<TL_PD0_PWM0N
- <<TL_PB2_KEY1
- <<TL_PB1_KEY2
- <<TL_PB0_KEY3
- <<TL_PB6_MODE
- <<TL_PA7_SWS
- <<TL_PB4_VBUSDET
- <<TL_PA5_DM
- <<TL_PA6_DP
- <<TL_PB3_VBATDET
- <<TL_PE0_UART1_TX
- <<TL_PE2_UART1_RTX

