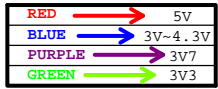
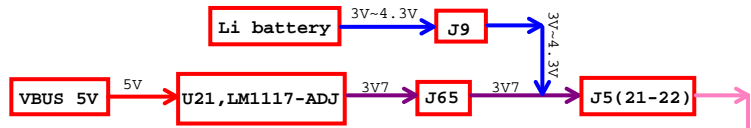
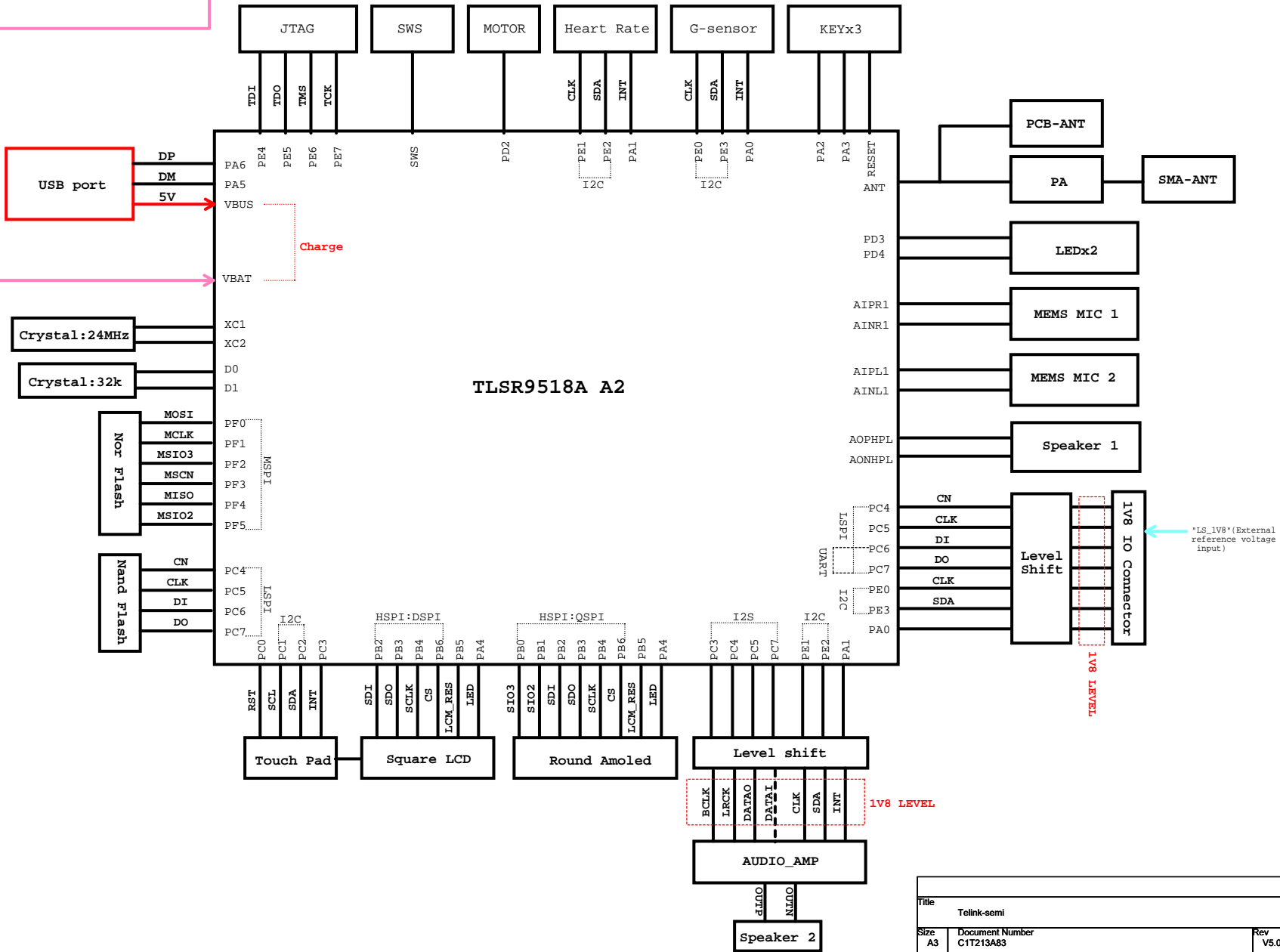


	Date	Note	Designer	Reviewer
REV1.0	2020-06-04	Initial Version	Hongwei	Jianbo
REV2.0	2020-06-22	Add PA chip GC1101 Add GND test point, add jumper silkscreen Nand flash data PD2 PD3 change to PB6 PB7	Hongwei	Jianbo
REV3.0	2020-07-17	1. change of J5 peripheral power supply jumper mode 2. add J13/J14/J15/J17 for MCU power control 3. update symbol of TLSR9518A 4. update LCM spi I/F 5. remove PA circuit 6. change key circuit 7. change LED IO 8. change NAND IO 9. add a W25X10_20_40BL nor flash footprint 10. add flash reset button	Jianbo	
REV4.0	2020-08-10	1. remove J14/J15/J29/J46/S1/R21/R22/R27/R28/R26 2. remove PCB ANT, PD LED 3. add R52		
REV5.0	2020-11-12	1. remove J6. 2. Add GC1101and KCT8204L compatibility, Add PCB ANT. 3. Add PD5 for PA_BYPASS ctrl. 4. Add PD6 for PA_RXEN,PD7 for PA_TXEN . 5. Add J56 for circle LCD,Add four Res for select SPI model. 6. Add page7 AUDIO AMP. 7. Add Auidio_Amp TFA9894BUK/N1,And add Level Shift for Amp. 8. Add LevelShift for external IC connect 9518 by I2C/SPI. 9. Add J73 for UART DEBUG. 10. Add 1.8V LDO for TFA9894BUK/N1 and extern LS. 11. Add I2S for TFA9894BUK/N1 used PC3,PC4,PC5. 12. Add I2C for TFA9894BUK/N1 used PA1,PE1,PE2. 13. Add SPI for external IC used PC4,PC5,PC6,PC7. 14. Add I2C for external IC used PA0,PE0,PE3.	Dongtao	Jianbo

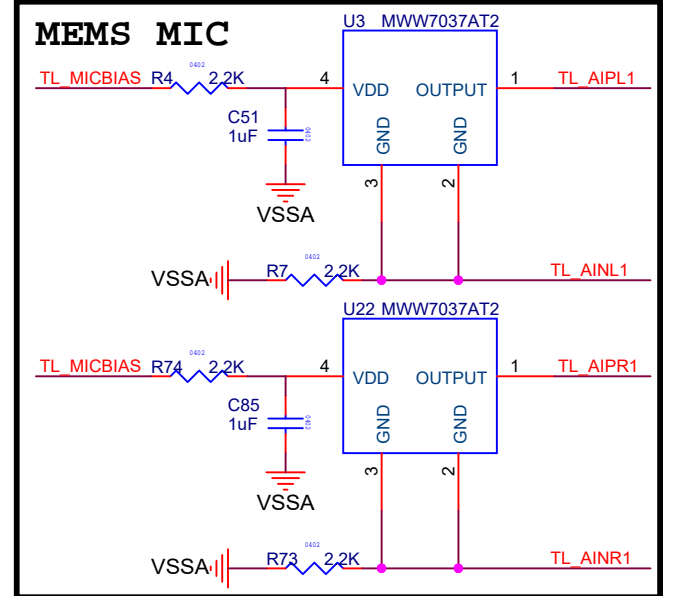
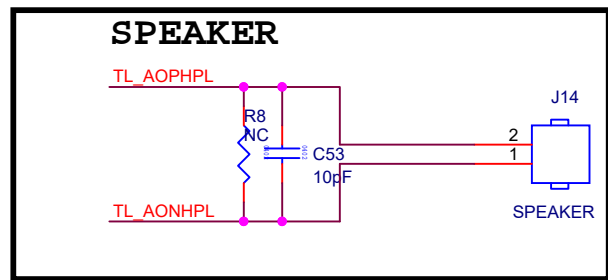
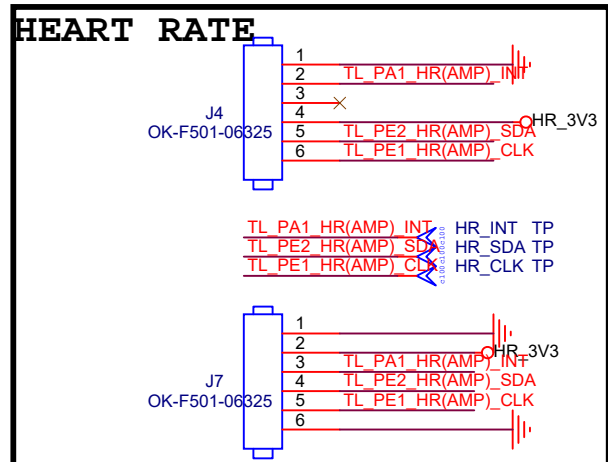
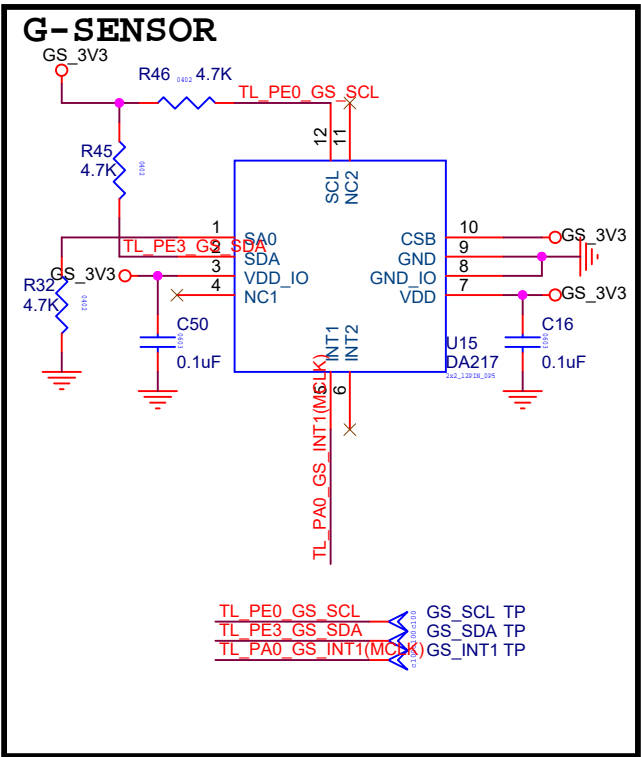
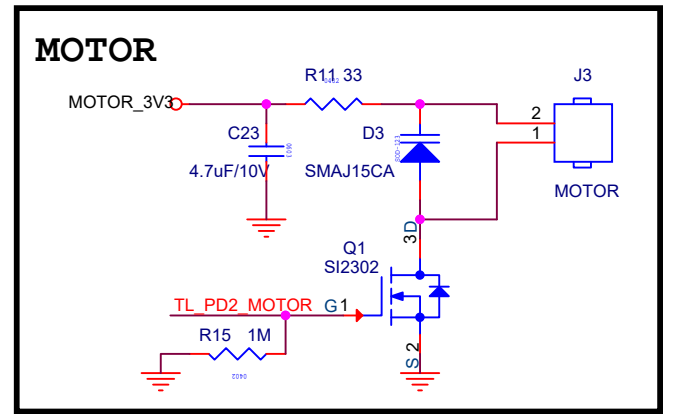
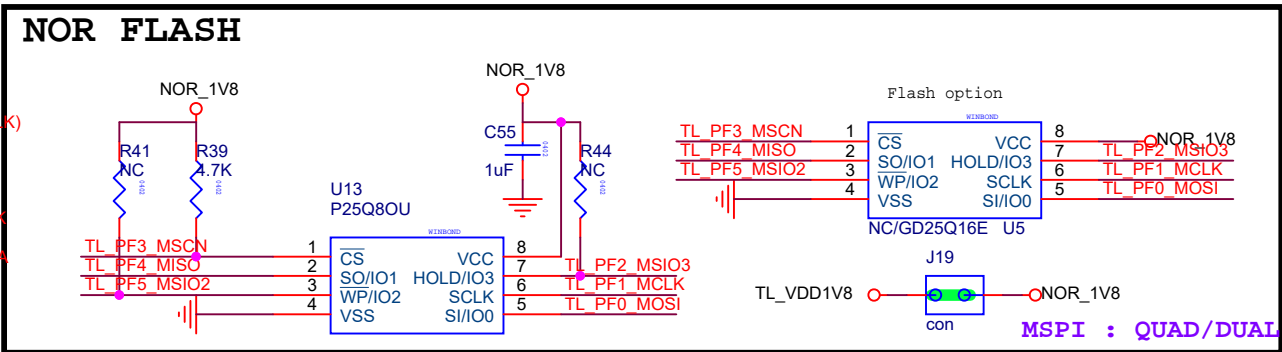
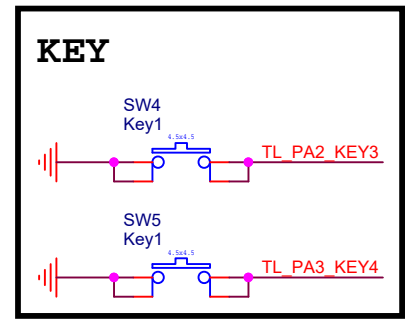
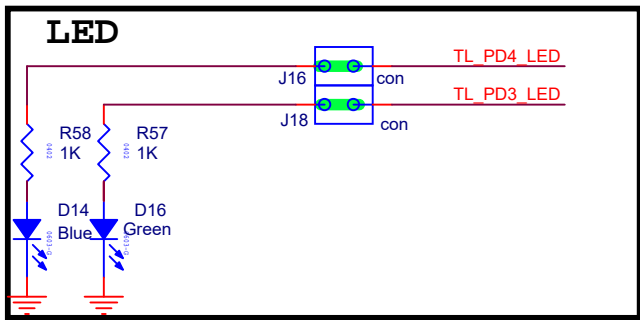
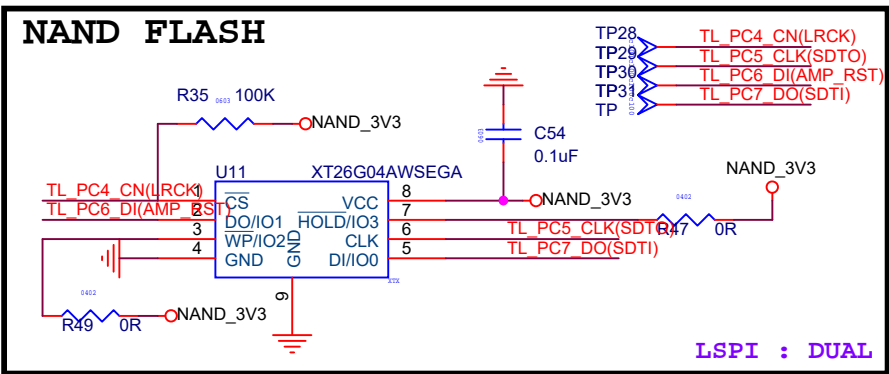
Title			
Telink-semi			
Size	Document Number		Rev
A4	C1T213A83		V5.0
Date:	Wednesday, December 23, 2020	Sheet	1 of 8



Hardware support:
I2C x1
HSPI x1
LSPI x1
MSPI x1
UART x2

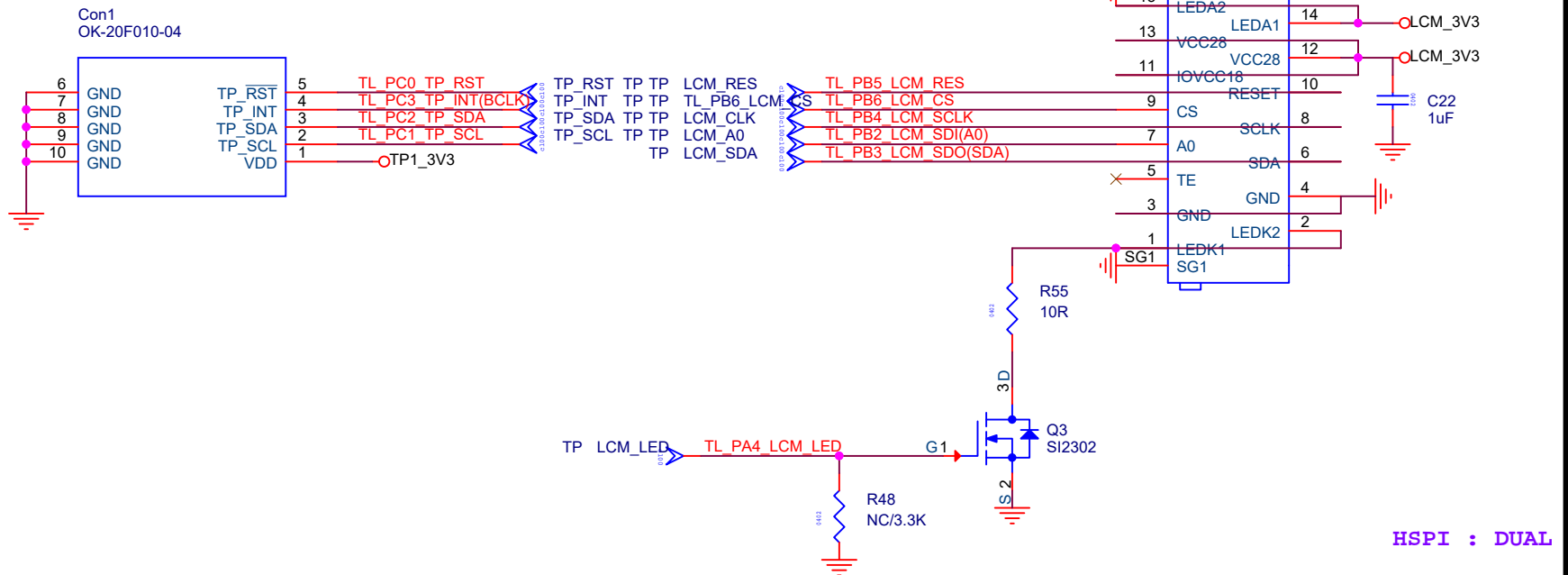


- <<TL_PC4_CN(LRCK)
- <<TL_PC5_CLK(SDTO)
- <<TL_PC6_DI(AMP_RST)
- <<TL_PC7_DO(SDTI)
- <<TL_PF5_MSIO2
- <<TL_PF4_MISO
- <<TL_PF3_MSCN
- <<TL_PF2_MSIO3
- <<TL_PF1_MCLK
- <<TL_PF0_MOSI
- <<TL_PE3_GS_SDA
- <<TL_PE0_GS_SCL
- <<TL_PA0_GS_INT1(MCLK)
- <<TL_PA1_HR(AMP)_INT
- <<TL_PA1_HR(AMP)_CLK
- <<TL_PA2_KEY3
- <<TL_PA3_KEY4
- <<TL_AOPHPL
- <<TL_AONHPL
- <<TL_MICBIAS
- <<TL_AINR1
- <<TL_AIPR1
- <<TL_AINL1
- <<TL_AIPL1
- <<TL_PD2_MOTOR
- <<TL_PD4_LED
- <<TL_PD3_LED

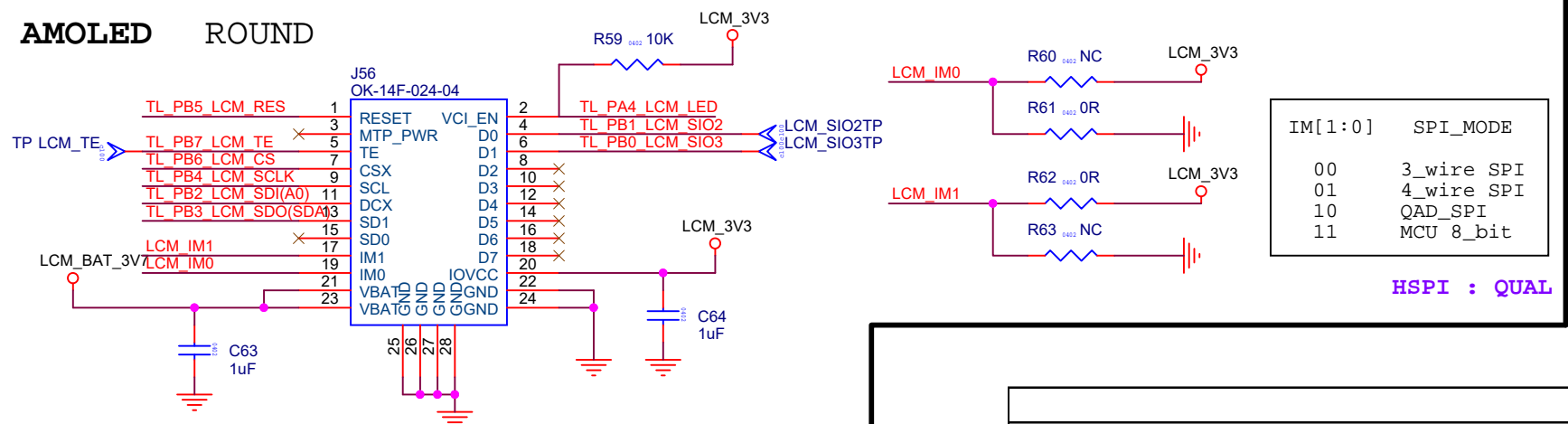


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Size	Document Number		Rev
A4	C1T213A83		V5.0
Date:	Wednesday, December 23, 2020		Sheet 4 of 8
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LCD+TP SQUARE



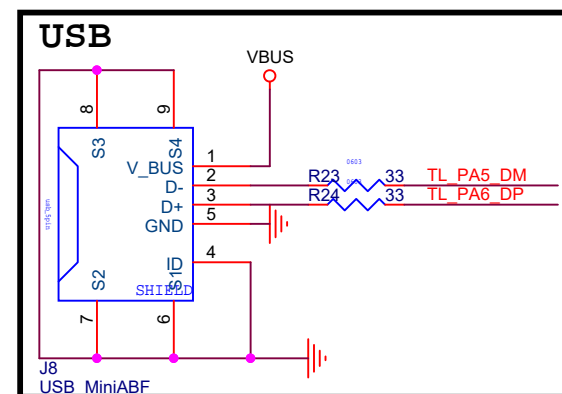
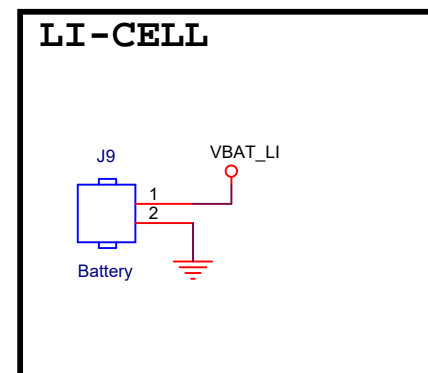
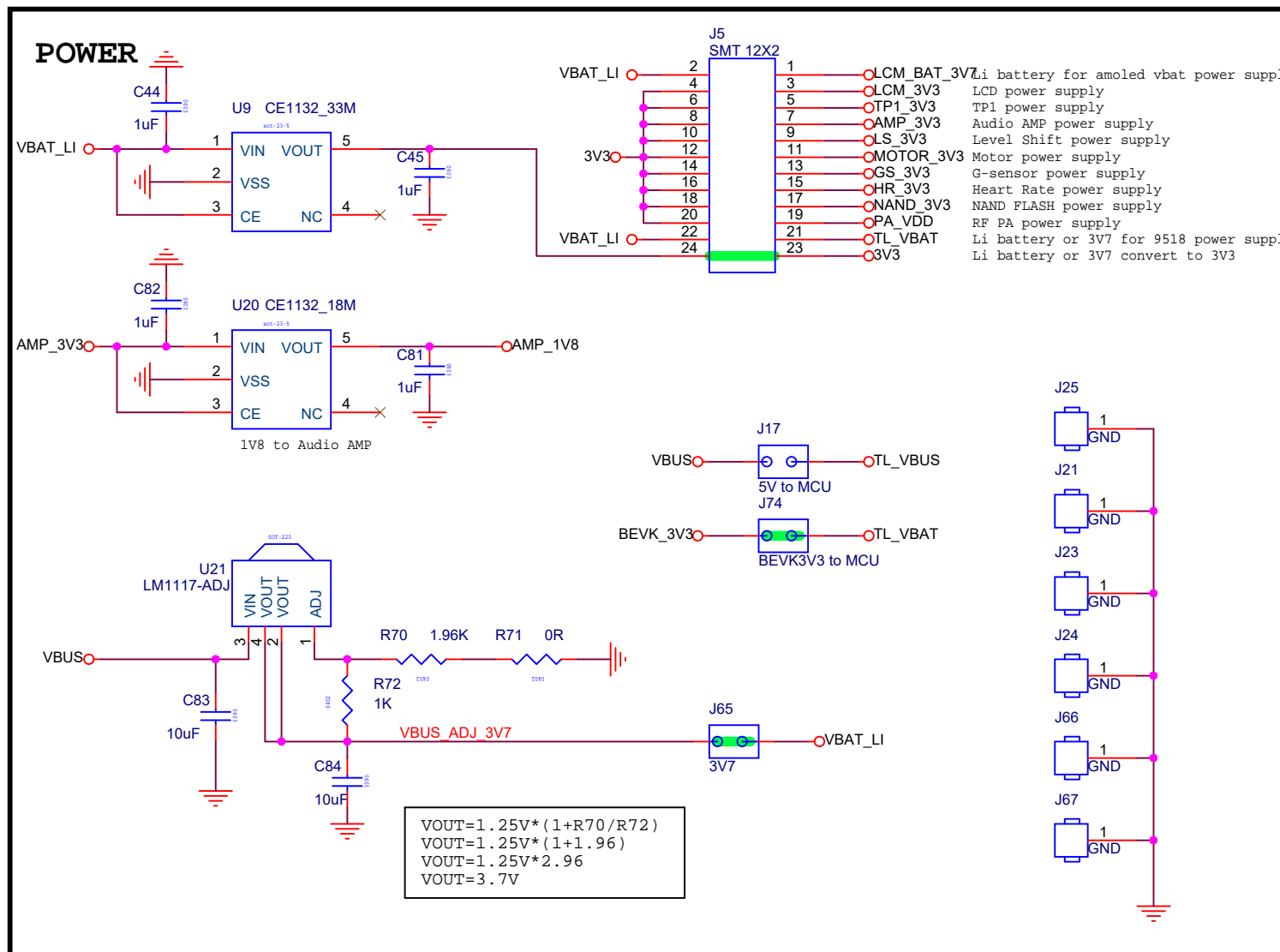
AMOLED ROUND



Title									
Telink-semi									
Size A4	Document Number C1T213A83								Rev V5.0
Date:	Wednesday, December 23, 2020				Sheet	5	of	8	

<<TL_PA5_DM

<<TL_PA6_DP

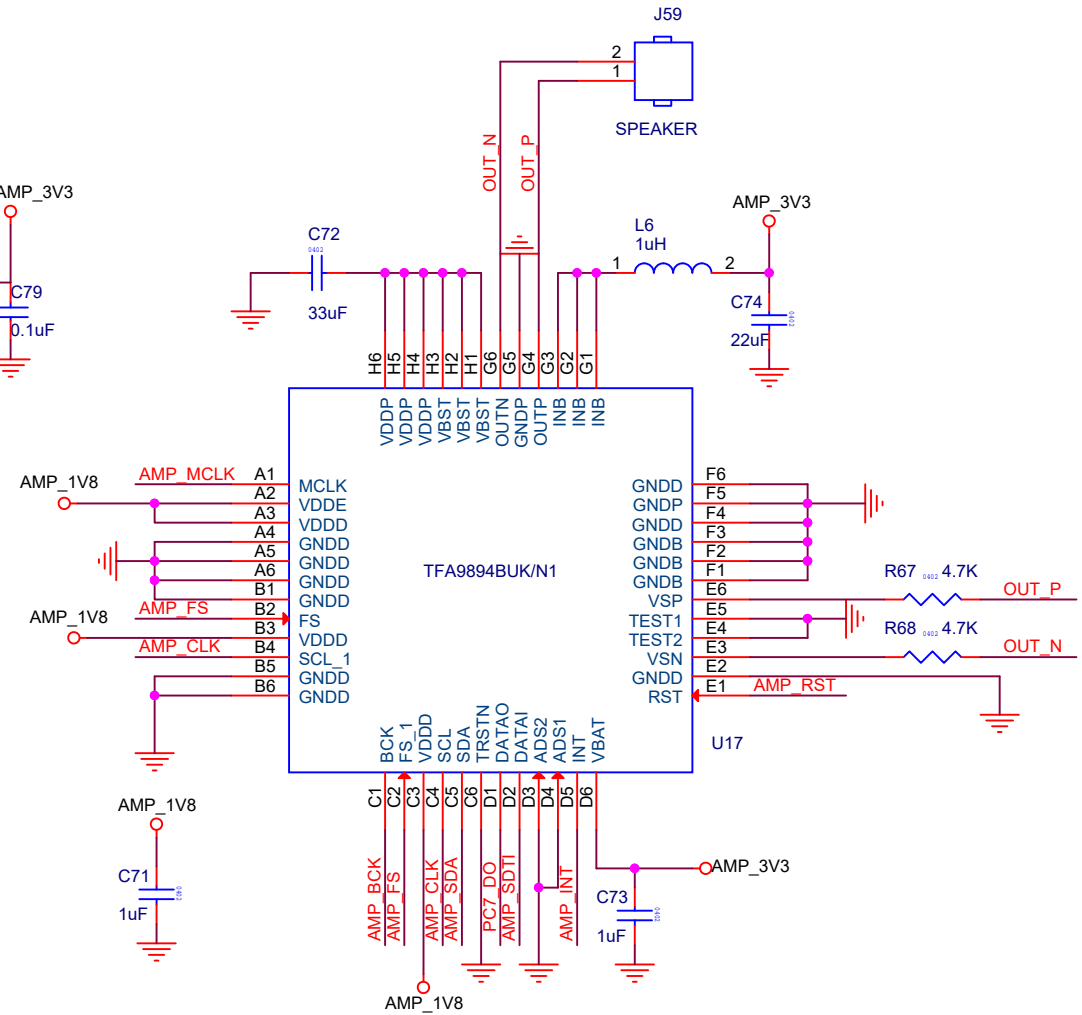
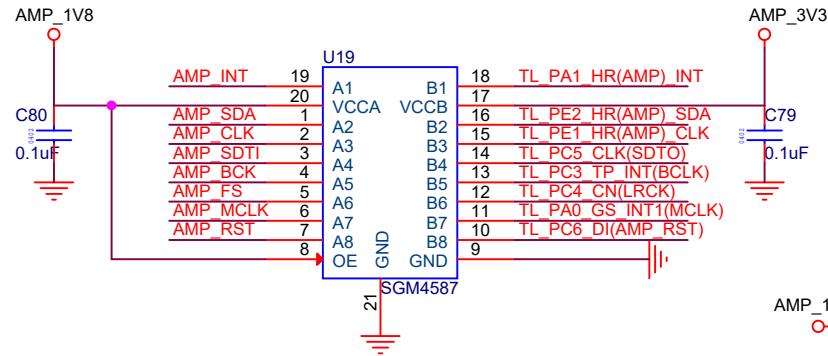


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Telink-semi		
Size	Document Number	Rev
A4	C1T213A83	V5.0
Date:	Wednesday, December 23, 2020	Sheet 6 of 8

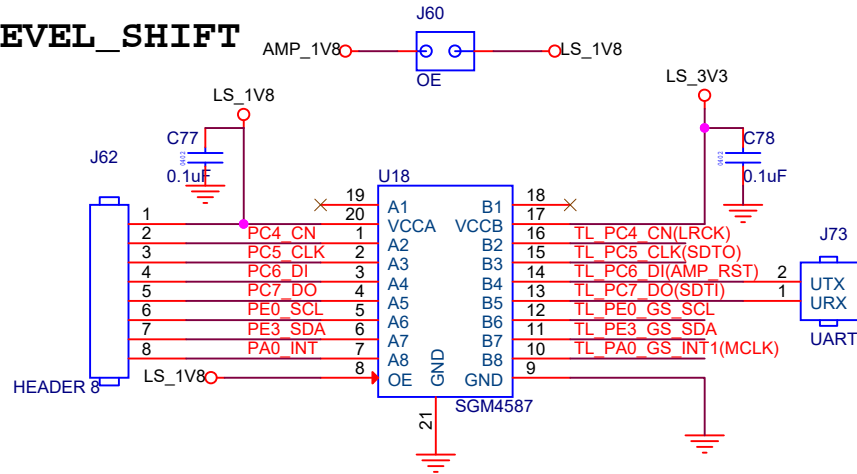
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Size A4	Document Number C1T213A83			Rev V5.0
Date:	Wednesday, December 23, 2020	Sheet	7 of 8	

<< TL_PA1_HR(AMP)_INT
 << TL_PE2_HR(AMP)_SDA
 << TL_PE1_HR(AMP)_CLK
 << TL_PC5_CLK(SDTO)
 << TL_PC4_CN(LRCK)
 << TL_PA0_GS_INT1(MCLK)
 << TL_PC6_DI(AMP_RST)
 << TL_PC7_DO(SDTI)
 << TL_PC3_TP_INT(BCLK)
 << TL_PE0_GS_SCL
 << TL_PE3_GS_SDA
 << TL_PA0_GS_INT1(MCLK)

AUDIO_AMP



LEVEL_SHIFT



TLRS9518/TFA9894 I2C connection

TLRS9518(Master) TFA9894
 TL_PE2_HR(AMP)_SDA <--> AMP_SDA
 TL_PE1_HR(AMP)_CLK <--> AMP_CLK

TLRS9518/TFA9894 I2S connection

TLRS9518(Master) TFA9894
 TL_PC5_SDATAO <--> DATAI
 TL_PC3_BCLK <--> BCK
 TL_PC4_LR_IO <--> FS

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Size	Document Number	Rev
A4	C1T213A83	V4.0
Date:	Wednesday, December 23, 2020	Sheet 8 of 8