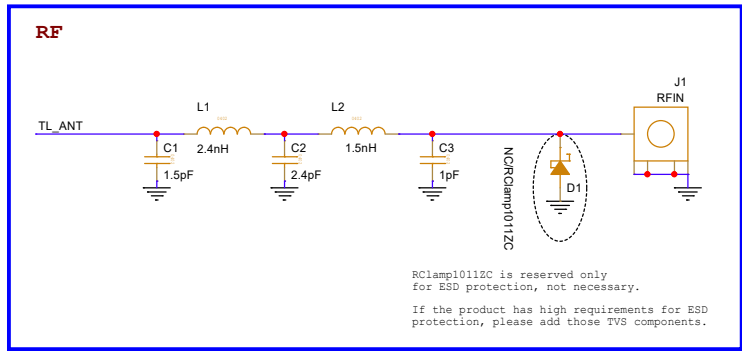
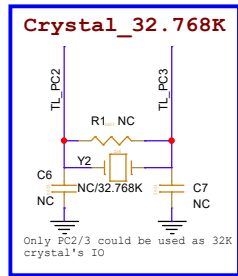
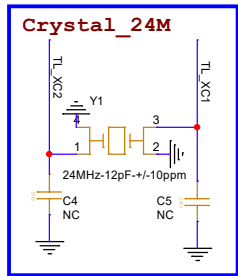


REV1.0

Date	Note	Author	Approved by
2025/07/10	Initial Version	QIANG	WeiXiang
2025/11/11	Redesigned PCB for internal use.	Tianze	WeiXiang
2026/03/17	Added 2 capacitors to reduce Rx leakage based on Ver 1.1. Solved some issues in Ver 1.1. Did NOT changed RF part.	Tianze	WeiXiang

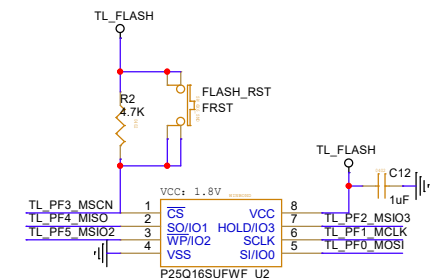
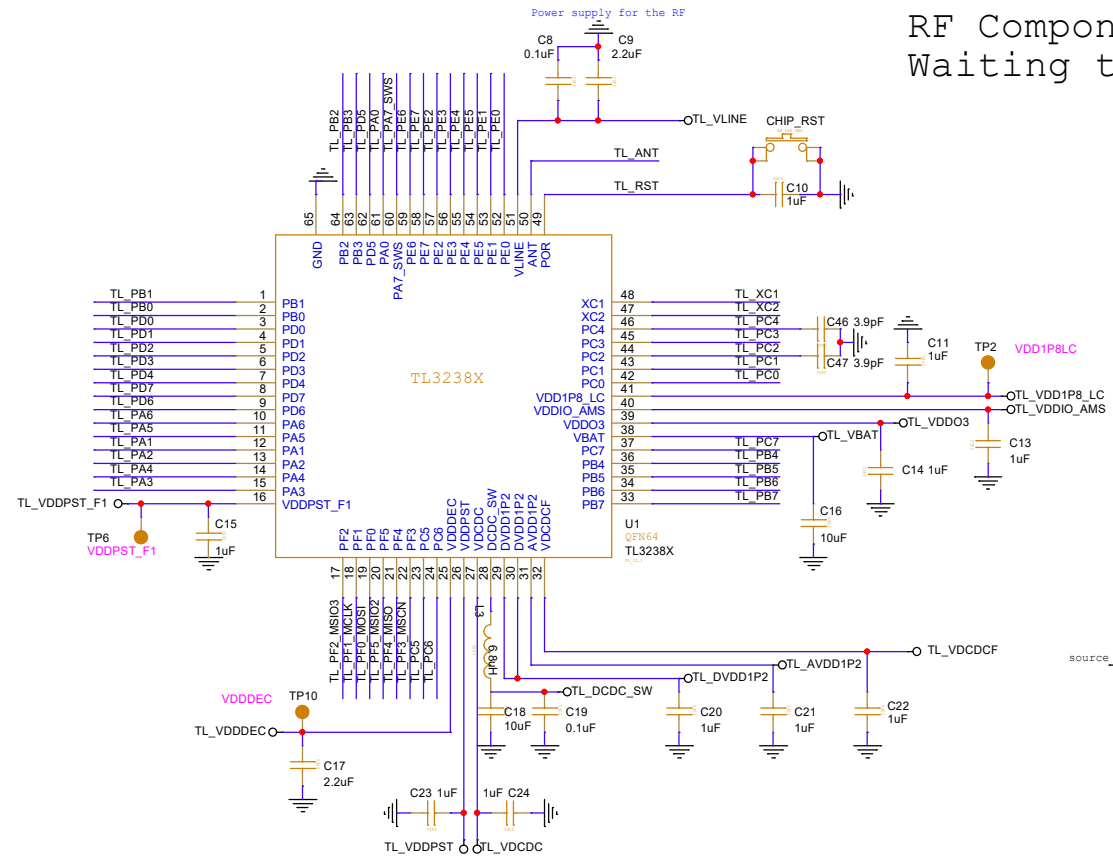
REV1.1

REV1.2

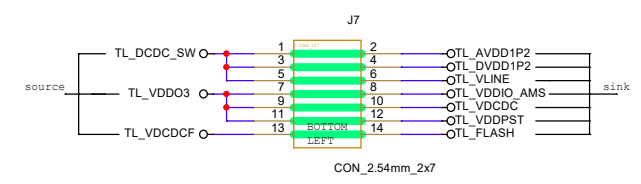


RF Components Update Notic
Waiting to be verified

- 4 << TL_PE7
- 4 << TL_PE6
- 4 << TL_PE5
- 4 << TL_PE4
- 4 << TL_PE3
- 4 << TL_PE2
- 4 << TL_PE1
- 4 << TL_PE0
- 4 << TL_PA7_SWS
- 4 << TL_PA6
- 4 << TL_PA5
- 4 << TL_PA4
- 4 << TL_PA3
- 4 << TL_PA2
- 4 << TL_PA1
- 4 << TL_PA0
- 4 << TL_PC7
- 4 << TL_PC6
- 4 << TL_PC5
- 4 << TL_PC4
- 4 << TL_PC3
- 4 << TL_PC2
- 4 << TL_PC1
- 4 << TL_PC0



External flash for TL3238X



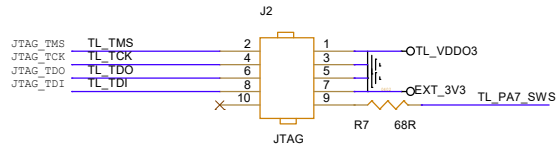
Chip internal power trace jumper.
Remove one of these jumper and
resist an ammeter to measure the
current on this trace.

VDD1P2 can be configured in either DCDC mode or LDO mode.
For DCDC mode, L3 must be soldered.
For LDO mode, L3 can be omitted, but the 10uF + 0.1uF capacitors at
the DVDD1P2 pin are mandatory.

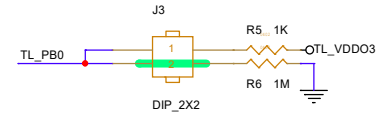
<Variant Name>			
Title			
TELINK			
Size	Document Number		Rev
A3	C1T388A20		V1.2
Date:	Tuesday, March 17, 2026		
	Sheet	3	of 5

Debug Interface

JTAG&SWS



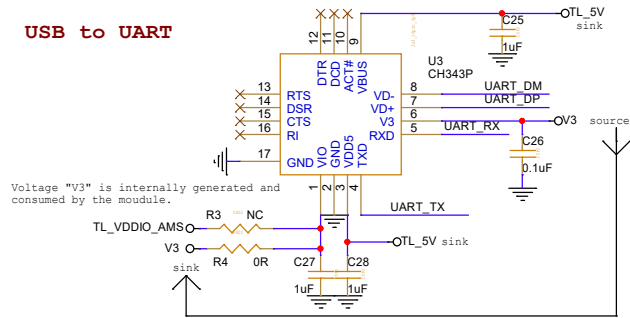
JTAG option



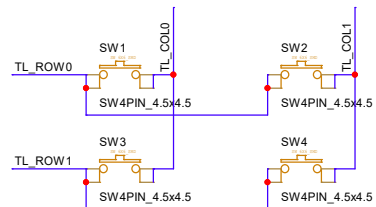
1	2	Mode
ON	OFF	2-wire
OFF	ON	4-wire
ON	ON	2-wire
OFF	OFF	4-wire

Functional Interface

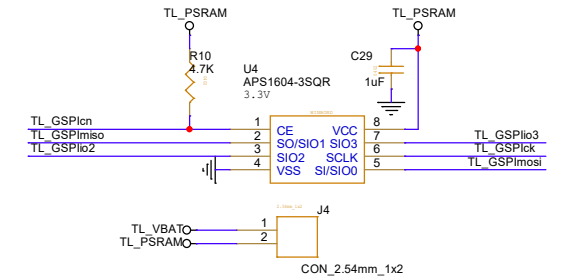
USB to UART



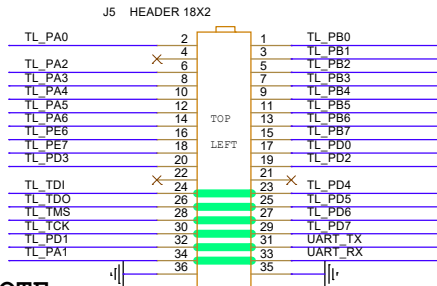
KEY



GSPI PSRAM

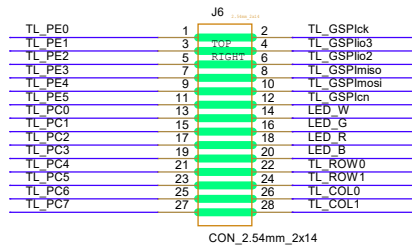


GPIO Header

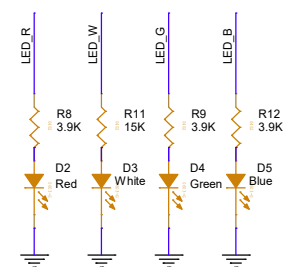


NOTE :

For telink chip. Software should configure "PD1" as RX pin; while "PA1" as TX pin



LED



<Variant Name>

Title				
TELINK				
Size A3	Document Number C1T388A20			Rev V1.2
Date:	Tuesday, March 17, 2026		Sheet	4 of 5

Size

Size A3	Document Number C1T388A20
------------	------------------------------

Size A3	Document N C1T388A20
------------	-------------------------

Date: Tuesday, March 17, 2026

Sheet	4
-------	---

Rev
V1.2

