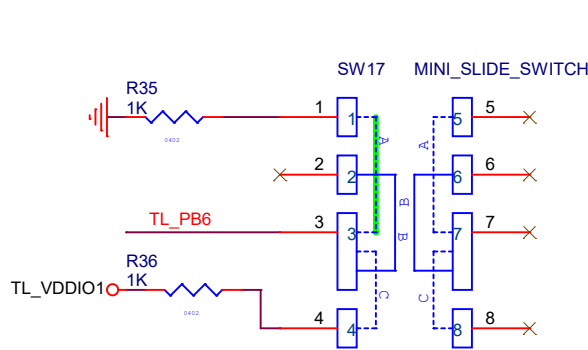
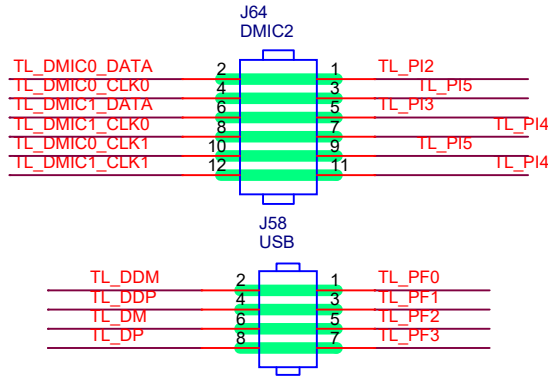
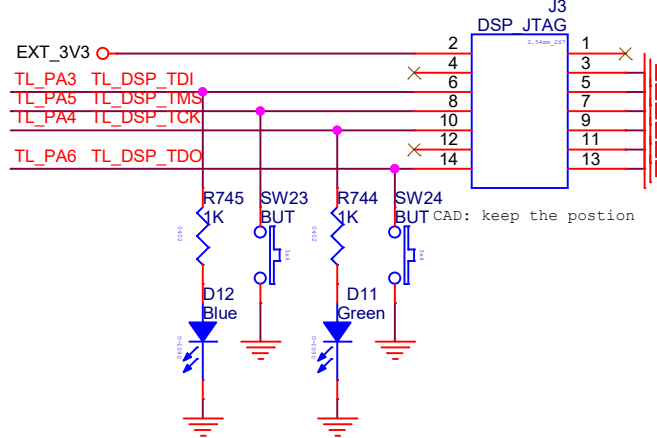


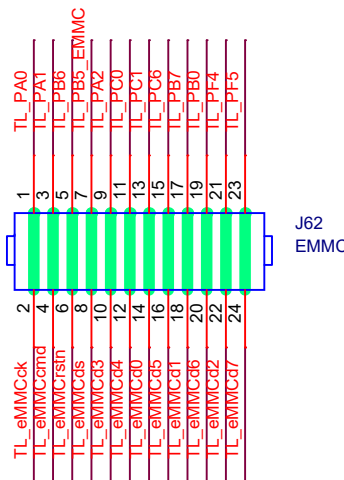
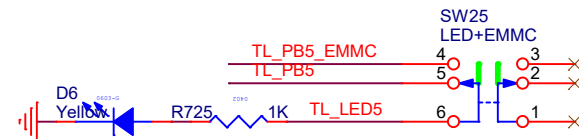
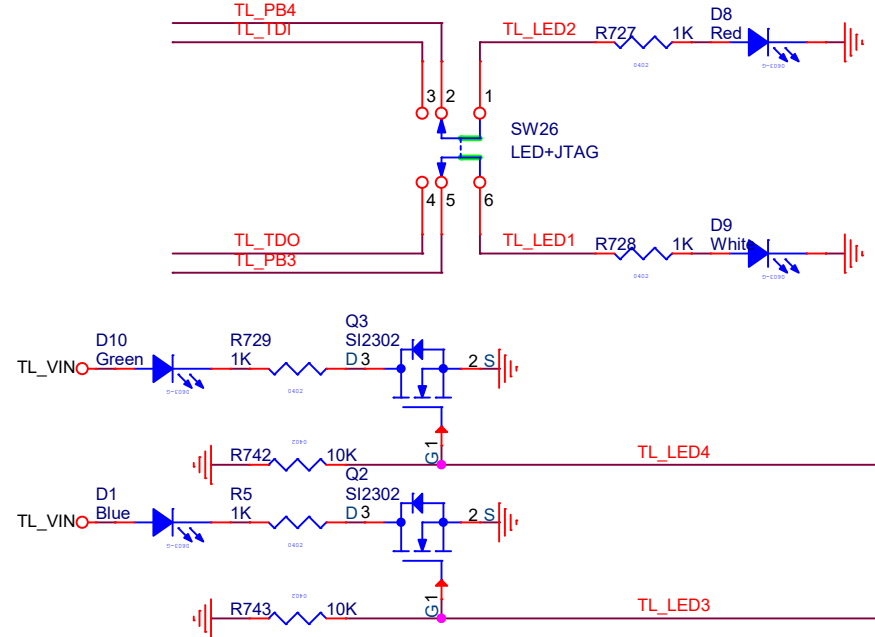
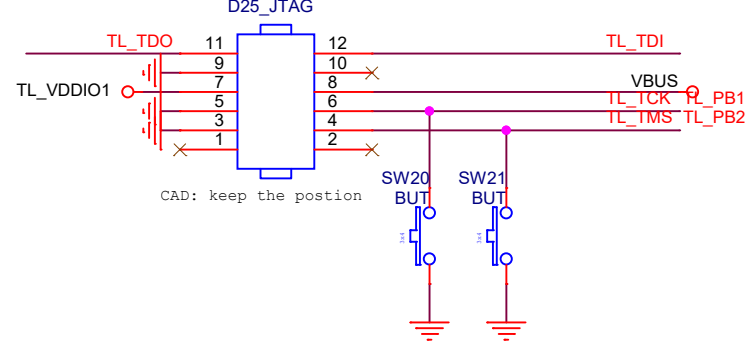
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| Title | | | Telink | | |
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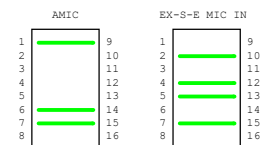
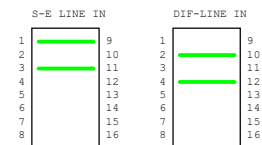
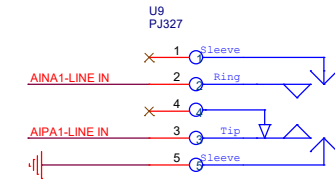
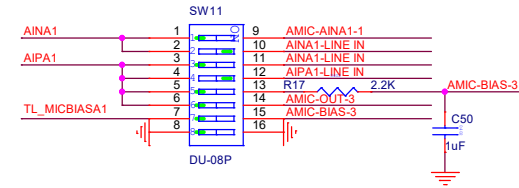
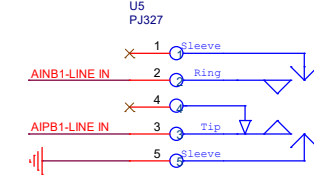
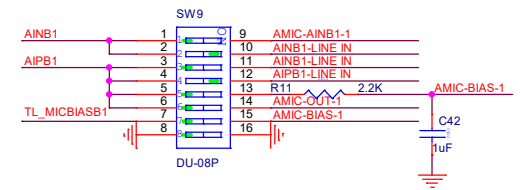
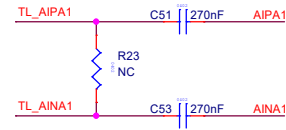
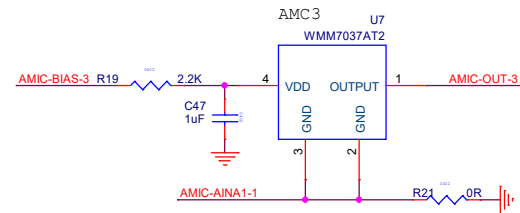
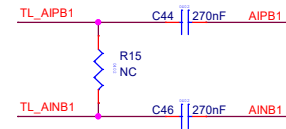
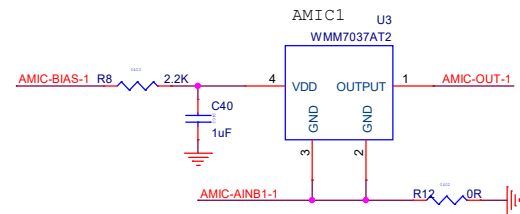
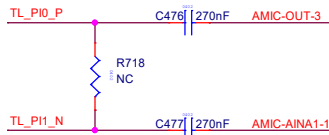
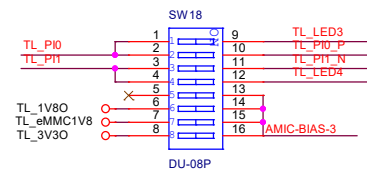
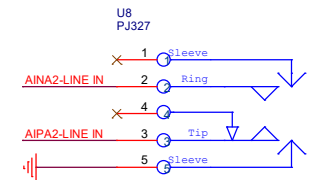
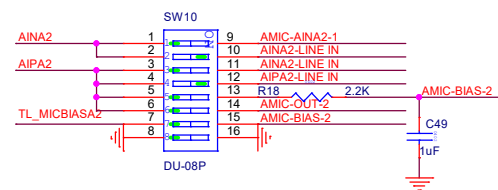
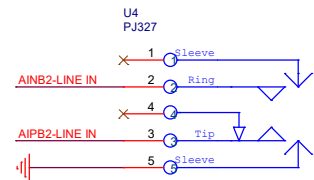
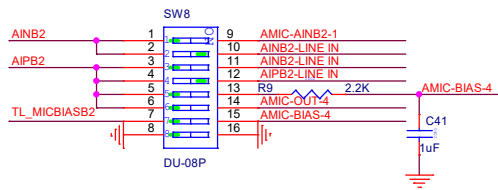
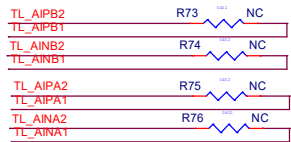
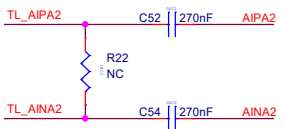
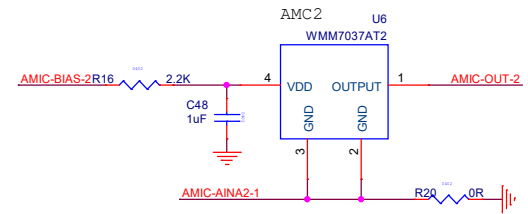
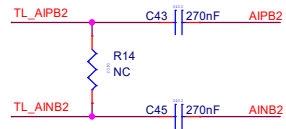
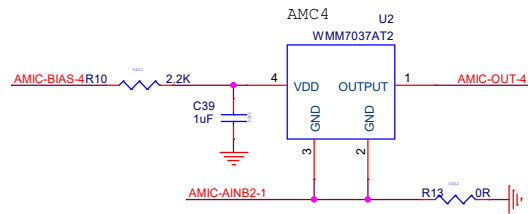


UP-LOW(Default): 4-Wire JTAG Mode
MIDDLE: NC
DOWN-HIGH: 2-Wire JTAG Mode

JTAG-D25



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>>>TL_MICBIASB2

>>>TL_AINB2

>>>TL_AIPB2

>>>TL_MICBIASB1

>>>TL_AINB1

>>>TL_AIPB1

>>>TL_MICBIASAZ

>>>TL_AINA2

>>>TL_AIPA2

>>>TL_MICBIASAZ

>>>TL_AINA1

>>>TL_AIPA1

>>>TL_PI0

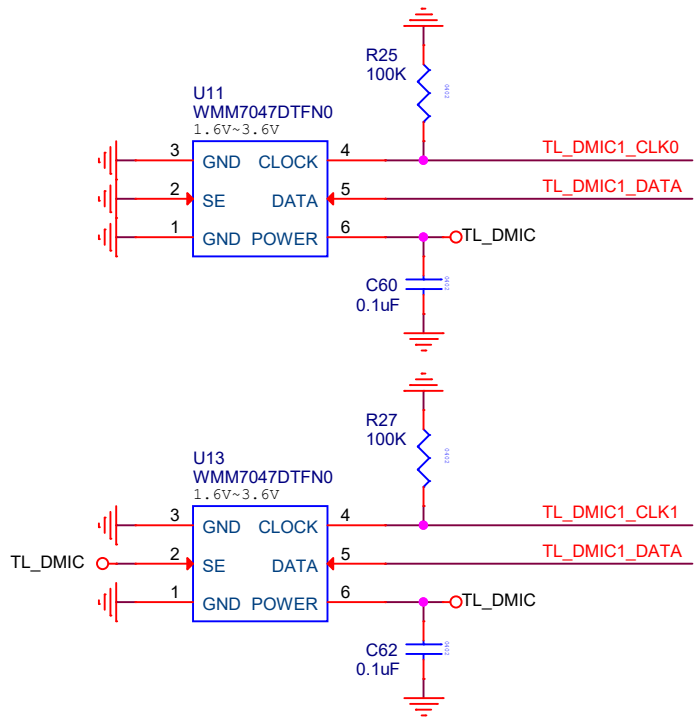
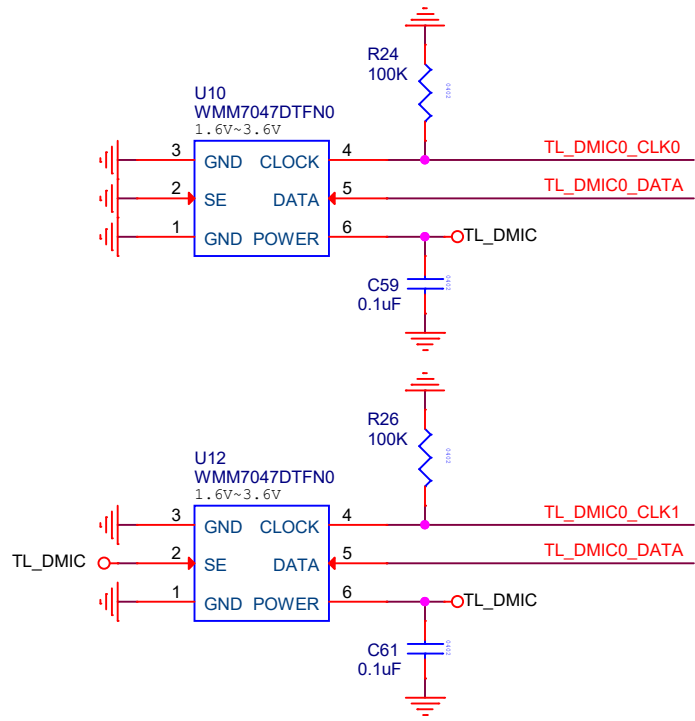
>>>TL_PI1

>>>TL_LED3

>>>TL_LED4

>>>TL_DMIC0_CLK0
>>>TL_DMIC0_CLK1
>>>TL_DMIC0_DATA

>>>TL_DMIC1_CLK0
>>>TL_DMIC1_CLK1
>>>TL_DMIC1_DATA



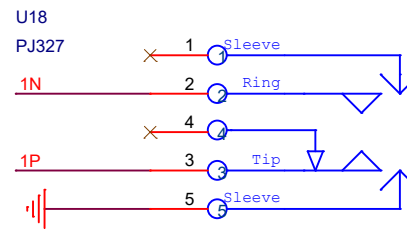
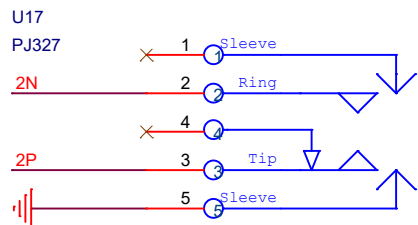
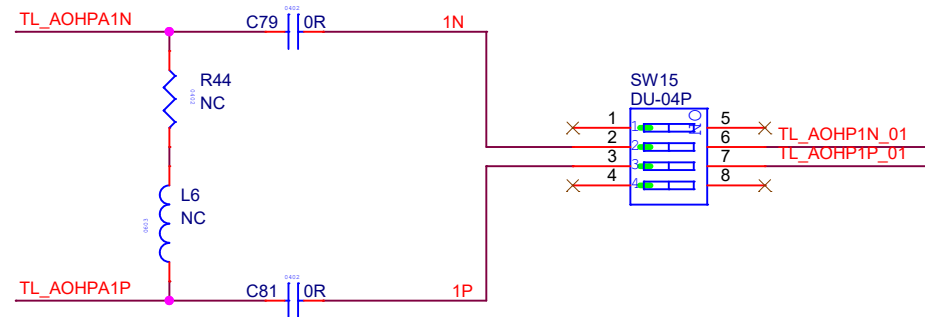
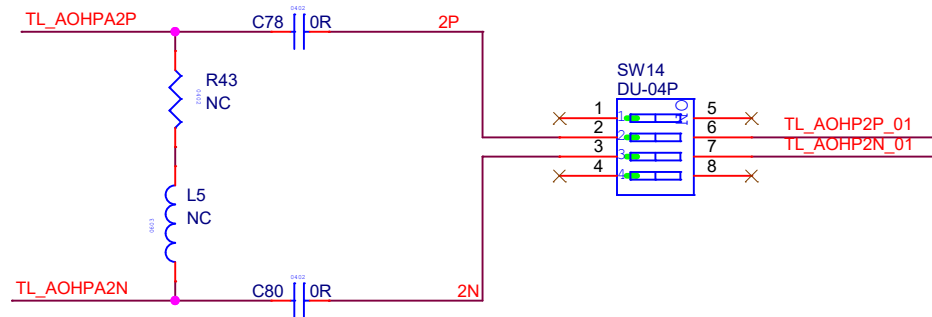
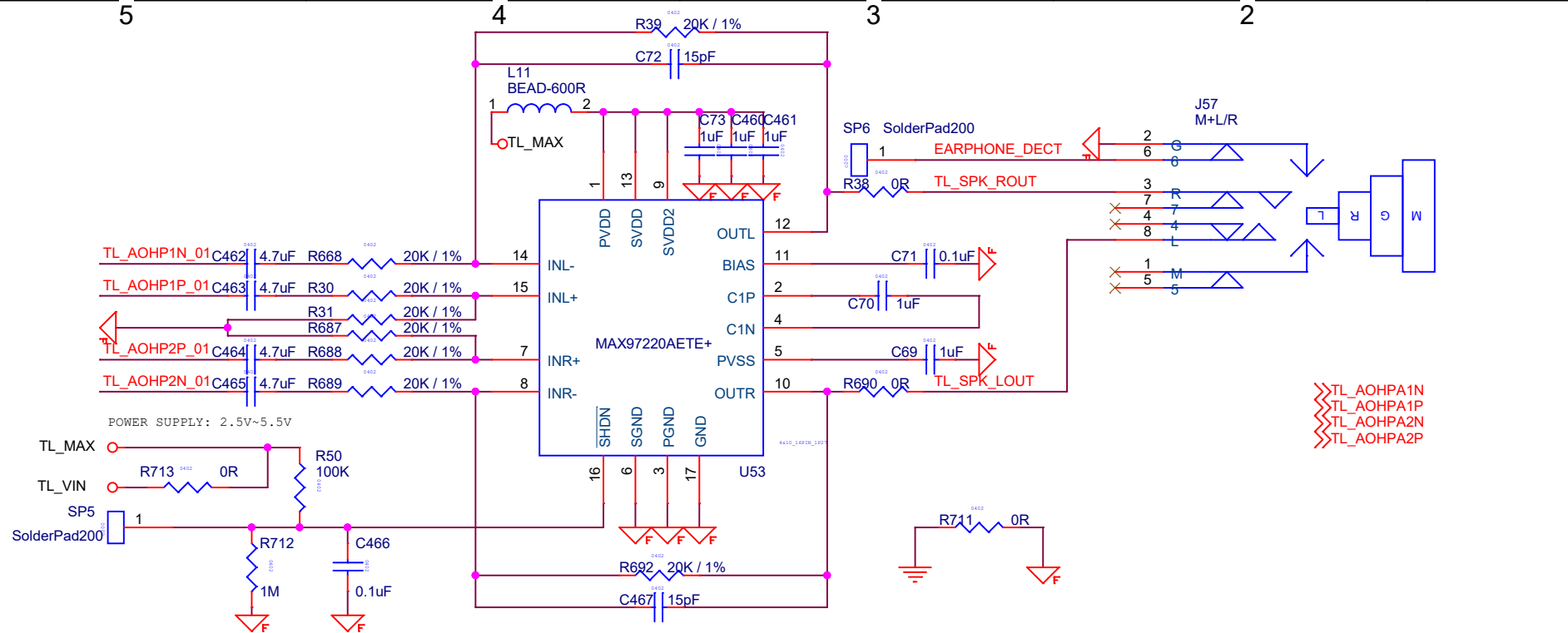
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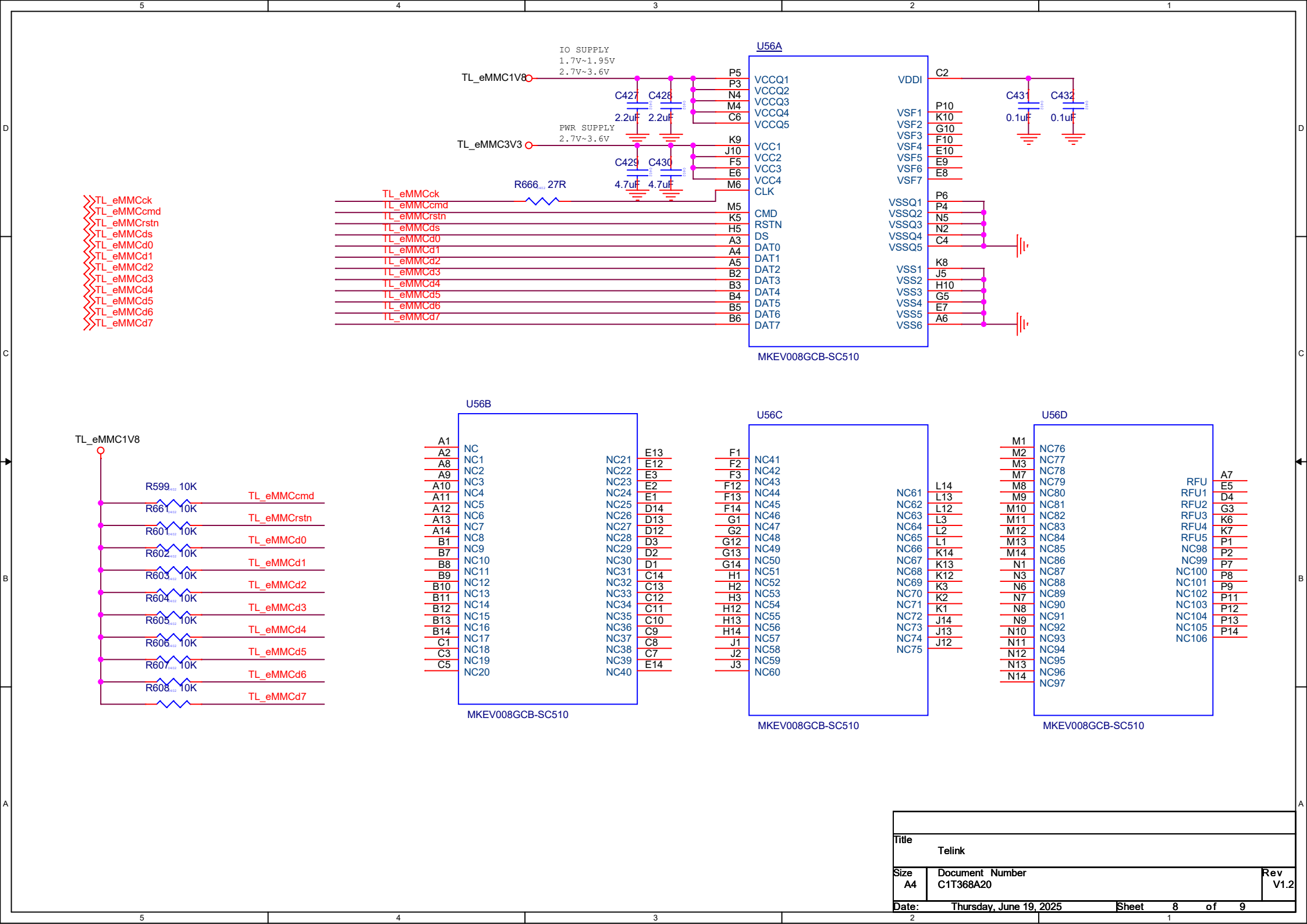
D

C

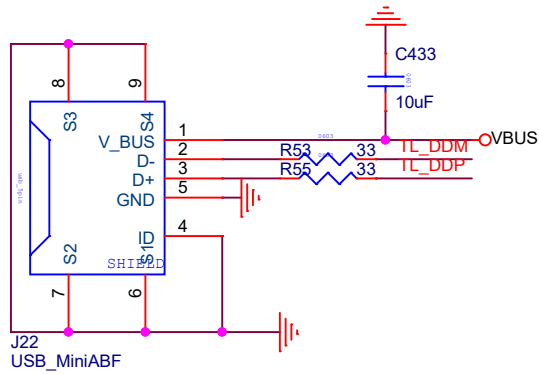
B

A

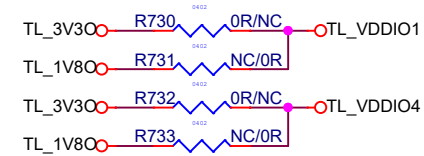
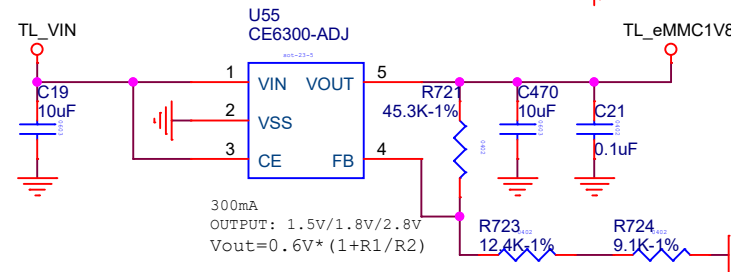
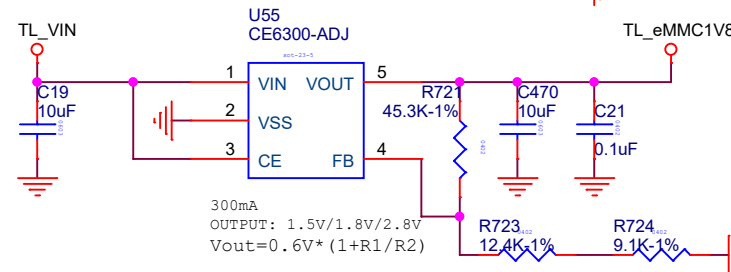
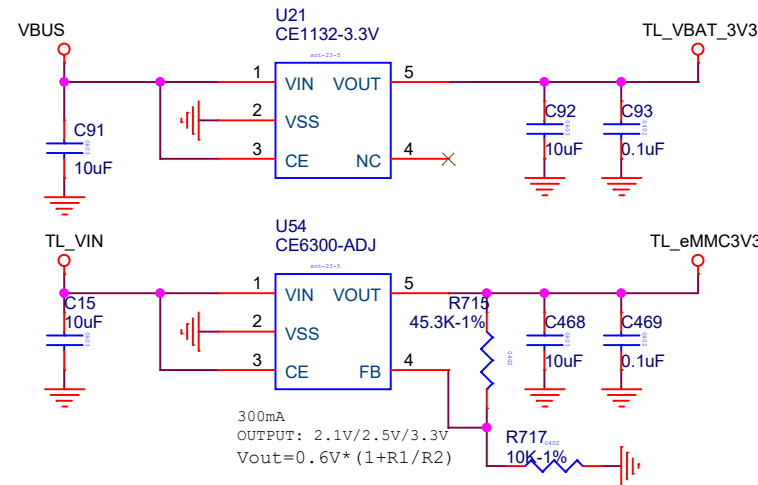
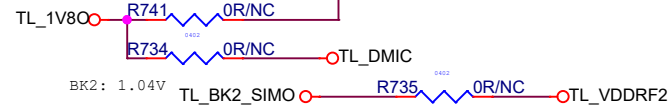
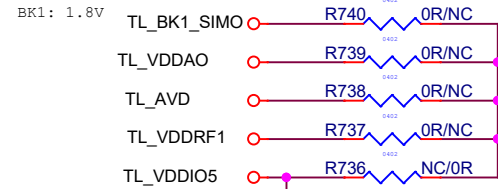
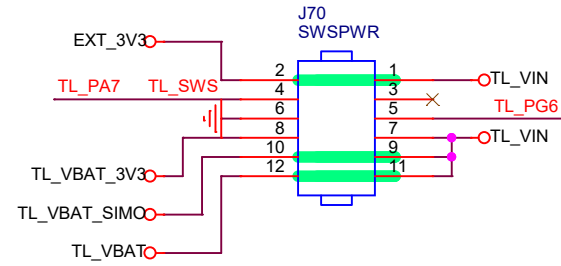
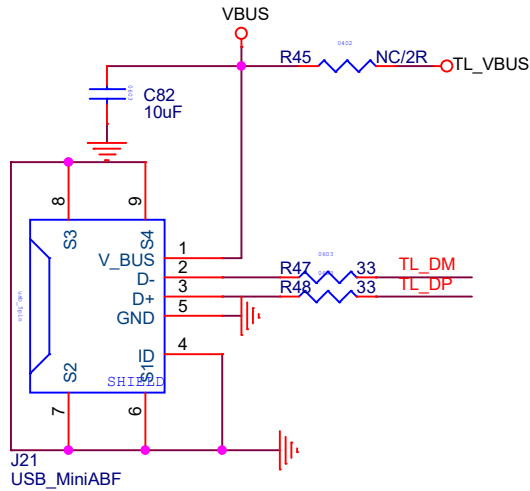




Debug USB



PWR USB



>>TL_DDM
>>TL_DDP
>>TL_DM
>>TL_DP
>>TL_PA7
>>TL_PG6

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