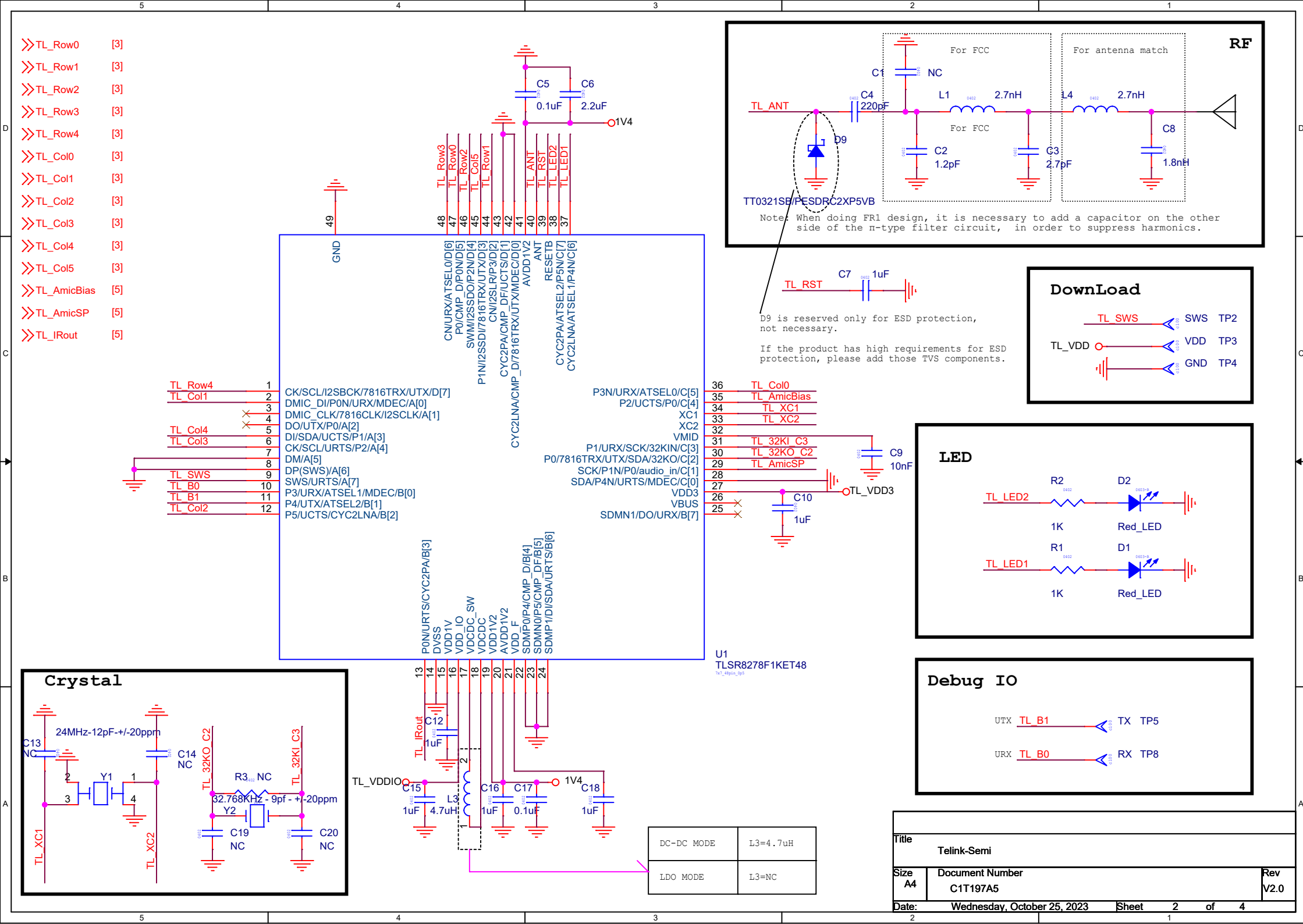
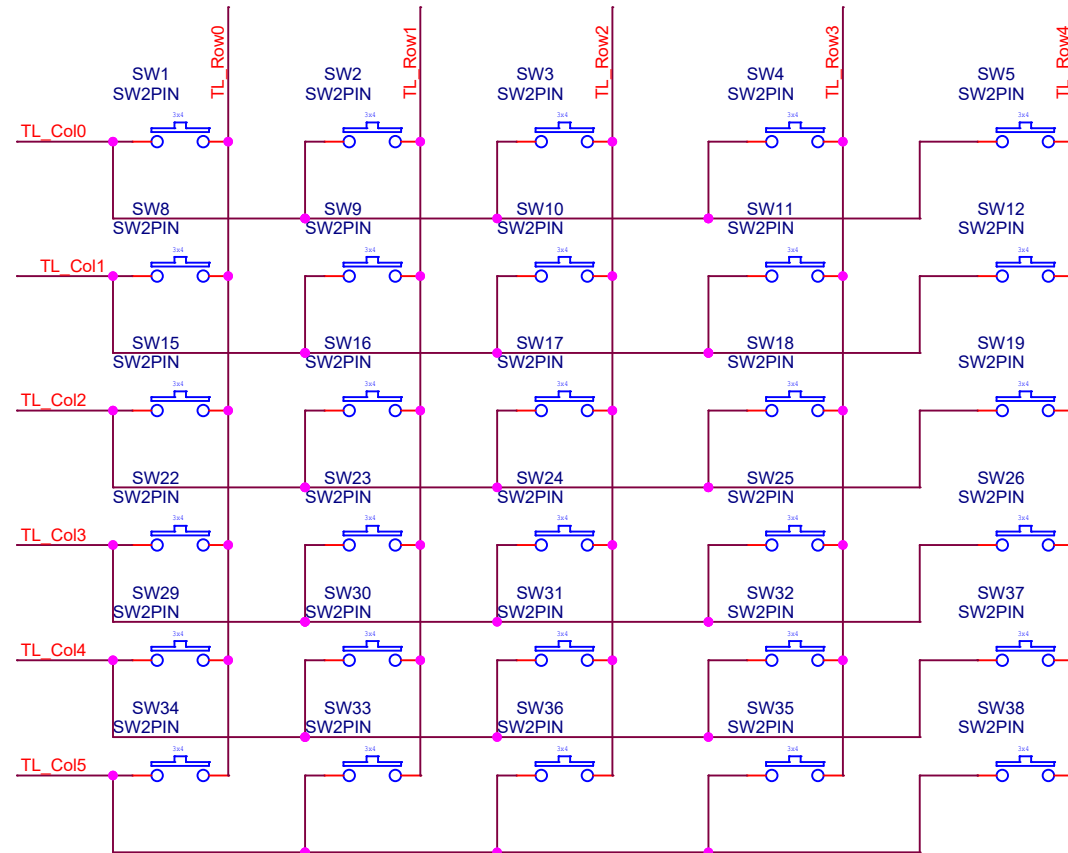


Date	Version	Note	Designer	Reviewer
2019-10-28	V2.0	Initial Version	Hongwei	Lixiang
2019-11-26	V2.0	Update value:C2=1.2pF L1=2.7nH C3=2.7pF R6=0R C8=1.8nH Chip lot:TLSR8278F1KET48 ZHU1933 EP7167.71	Hongwei	Lixiang
2021-07-12	V2.0	Add TVS diode TT0321SB to the RF pin, PCB is not updated	Hongwei	Lixiang
2023-10-09	V2.0	Add TVS diode TT0321SB/PESDRC2XP5VB to the RF pin, PCB is not updated Update value:L3=4.7uH	QIANG	Weixiang

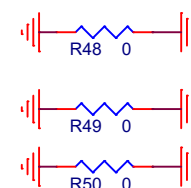
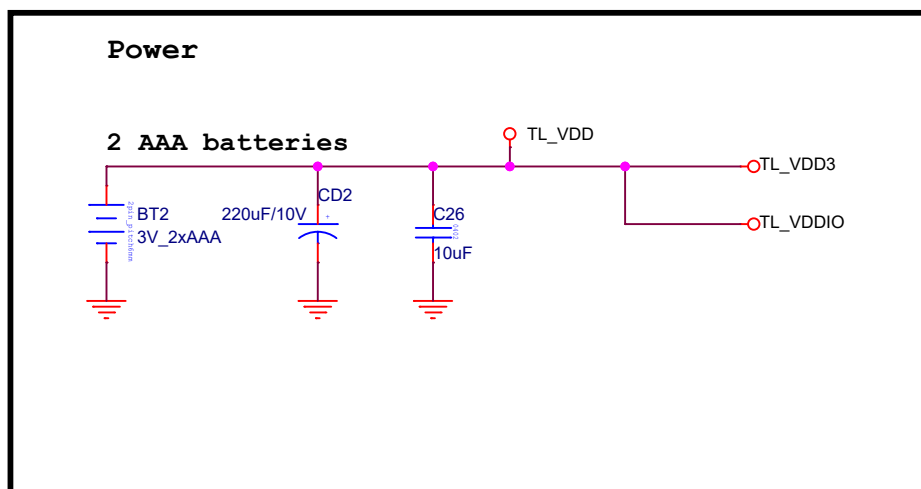
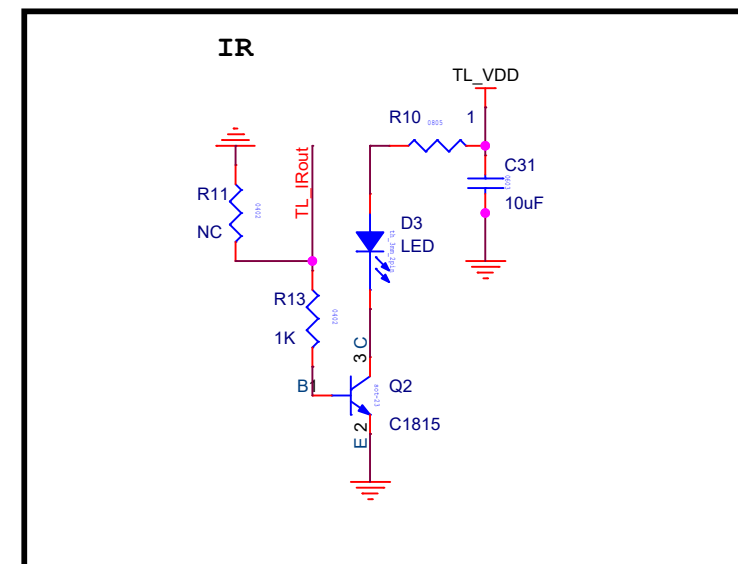
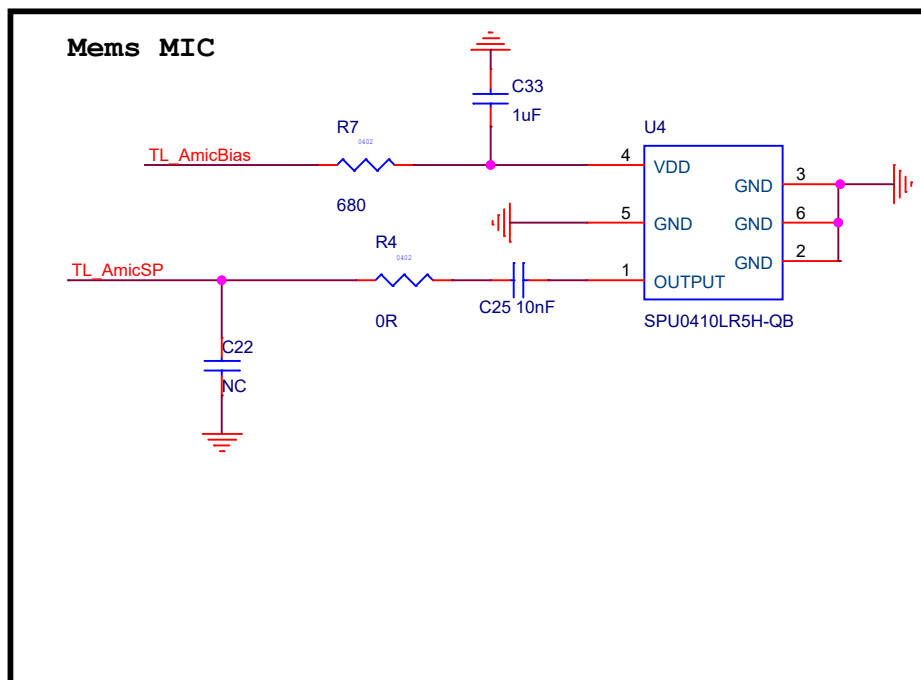
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Telink-Semi			
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- >>TL_Row0 [2]
- >>TL_Row1 [2]
- >>TL_Row2 [2]
- >>TL_Row3 [2]
- >>TL_Row4 [2]
- >>TL_Col0 [2]
- >>TL_Col1 [2]
- >>TL_Col2 [2]
- >>TL_Col3 [2]
- >>TL_Col4 [2]
- >>TL_Col5 [2]



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>>TL_AmicBias [2]
>>TL_AmicSP [2]
>>TL_IRout [5]
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Title				
Telink-Semi				
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